

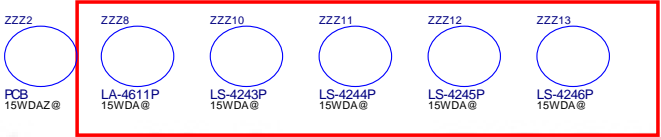
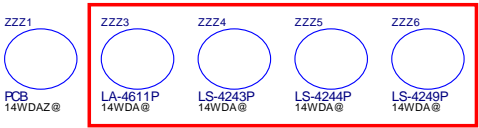
Compal Confidential

KSW01/91 Schematics Document

Intel Merom Processor with SiSM672/FX + DDRII + SiS968 + SiS307ELV

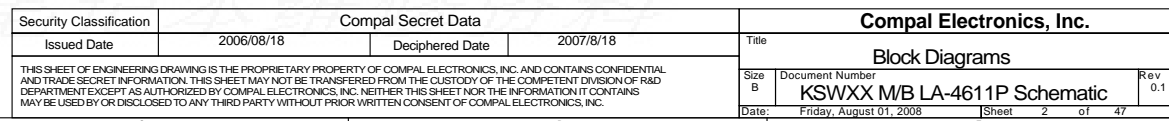
2008-08-01

REV: 0.2



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2008/05/18		Title	
		Deciphered Date		2009/05/18	
				Cover Page	
				Size B	Document Number
					KSWXX M/B LA-4611P Schematic
				Date:	Friday, August 01, 2008
				Sheet	1 of 47
				Rev	0.2

Model Name : KSW01/91
File Name : LA-4611P



Voltage Rails

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	NA	NA	NA
B+	AC or battery power rail for power circuit.	NA	NA	NA
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+0.9VS (Actual +0.9V)	0.9V switched power rail for DDR terminator	ON	ON	OFF
+1.05VS	1.05V switched power rail	ON	OFF	OFF
+1.2VS	1.2V switched power rail	ON	OFF	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8V	1.8V power rail for DDR	ON	ON	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

PROJECT ID Table

	PROJECT_ID
14W	R424 (Pull low)
15W	NA (Internal Pull High)

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

DEVICE	IDSEL #	REQ/GNT #	PIRQ
--------	---------	-----------	------

EC SM Bus1 address

Device	Address
Smart Battery	0001 011X b
EEPROM(24C16/02)	1010 000X b

EC SM Bus2 address

Device	Address
ADI ADM1032	1001 100X b

M672 SM Bus address

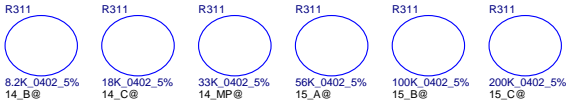
Device	Address
Clock Generator	1101 001Xb
DDR DIMM0	1010 000Xb
DDR DIMM1	1010 001Xb

SKU ID Table

Vcc	3.3V +/- 5%
Ra	100K +/- 5%

Ra~ R312

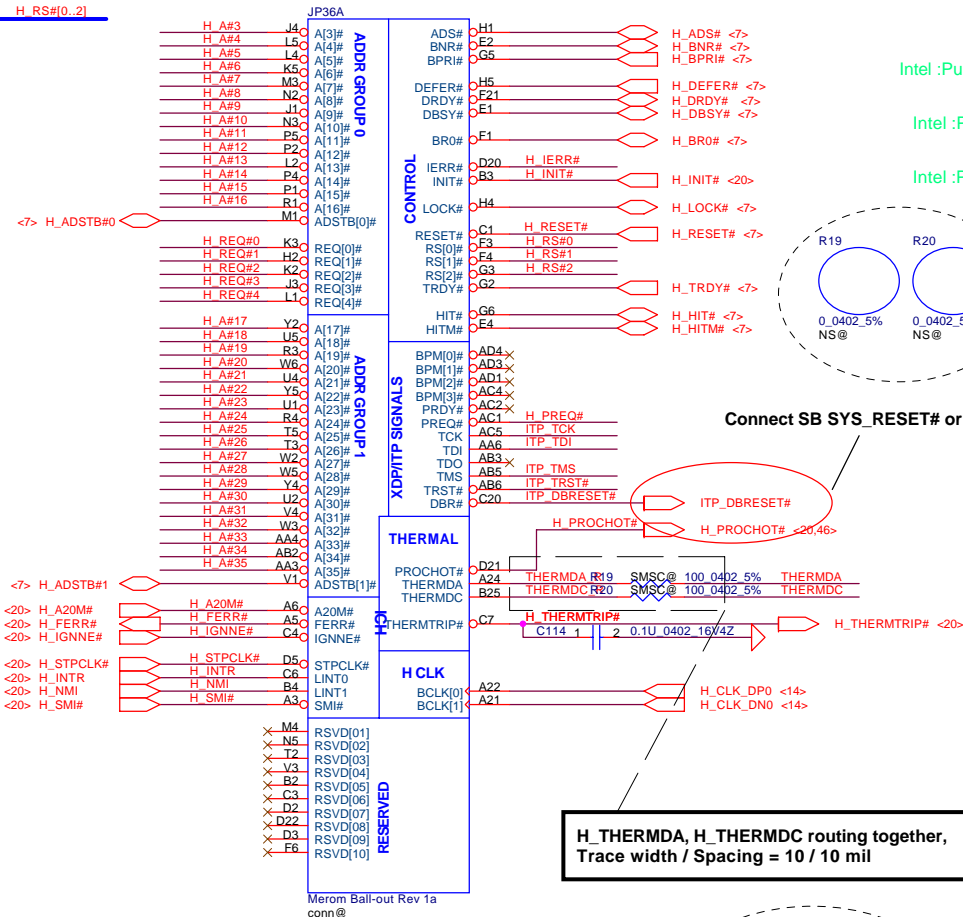
Rb~ R311



Board ID	Rb	VAD_BID min	VAD_BID typ	VAD_BID max	Rb BOM Structure
0	0	0 V	0 V	0 V	14_A@ ✓
1	8.2K +/- 5%	0.217 V	0.250 V	0.288 V	14_B@
2	18K +/- 5%	0.439 V	0.503 V	0.575 V	14_C@
3	33K +/- 5%	0.721 V	0.819 V	0.926 V	14_MP@
4	56K +/- 5%	1.054 V	1.185 V	1.325 V	15_A@ ✓
5	100K +/- 5%	1.489 V	1.650 V	1.819 V	15_B@
6	200K +/- 5%	2.019 V	2.200 V	2.386 V	15_C@
7	NC	3.135V	3.300 V	3.465 V	15_MP@

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Notes List		
Size B	Document Number KSWXX M/B LA-4611P Schematic					Rev 0.1
Date:	Friday, August 01, 2008					Sheet 3 of 47

<7> H_A# [3..35]
<7> H_REQ# [0..4]
<7> H_RS# [0..2]



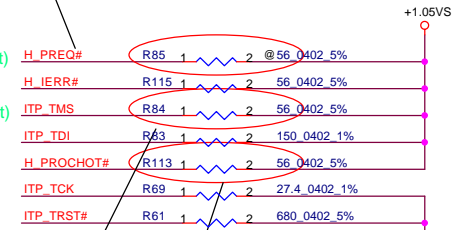
Trace length must short

Place close to CPU within 500mil

Intel :Pull-up 56ohm (Un-Mount) SiS : Pull-up 56ohm (Mount)

Intel :Pull-up 56ohm (Mount) SiS : Pull-up 54.9ohm (Mount)

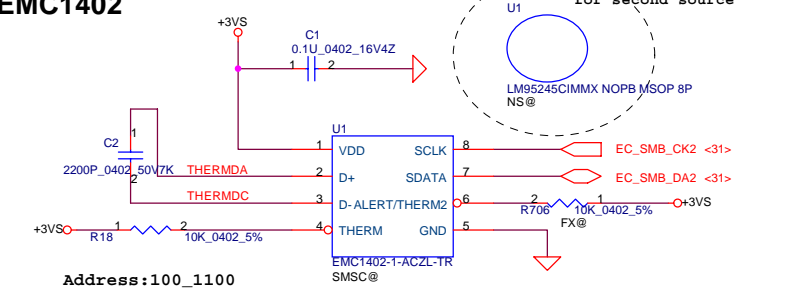
Intel :Pull-up 56ohm (Mount) SiS : Pull-up 75ohm (Mount)



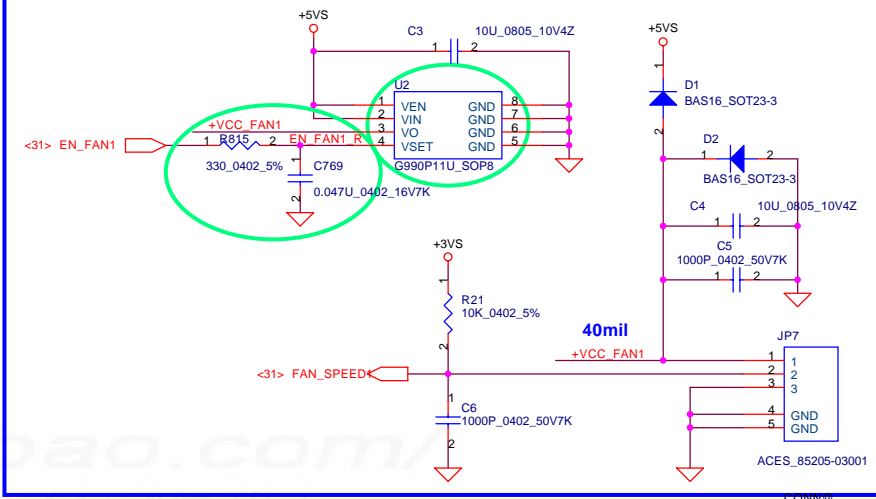
Checklist recommend 39 Ohm CRB pull 75 Ohm

EMC1402

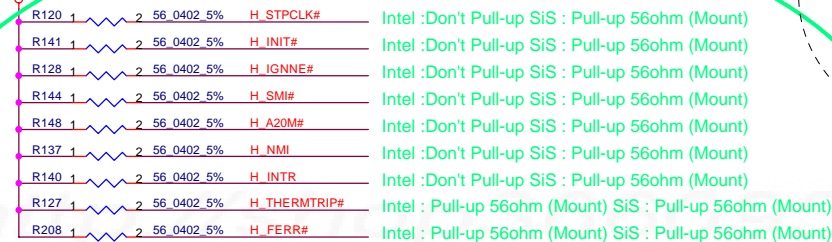
1/29 change to EMC1402 pn



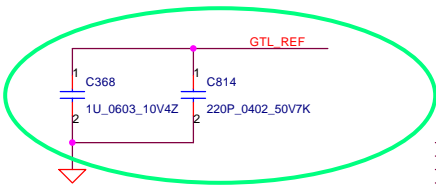
FAN1 Conn



CPU to SB interface

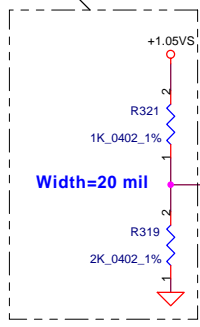


Security Classification		Compal Secret Data		Compal Electronics, Inc.				
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title				
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Merom (1/3)				
				Size B	Document Number		Rev	
					KSWXX M/B LA-4611P Schematic		0.1	
				Date:	Friday, August 01, 2008	Sheet	4	of

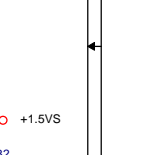
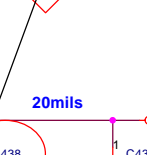
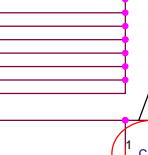
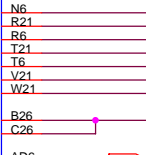
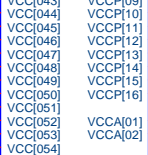
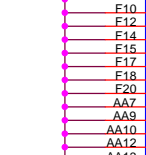
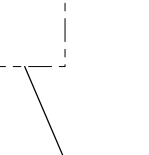
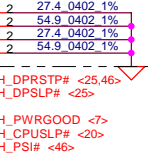
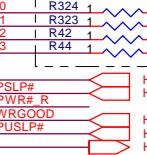
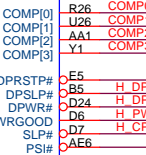
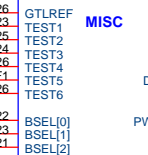
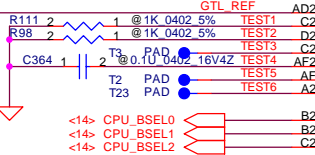


SiS Recommend

Close to CPU pin AD26 within 500mils.

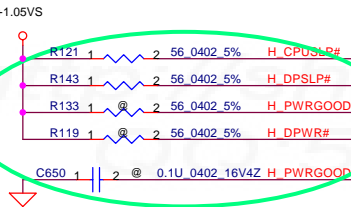


Width=20 mil



layout note: Route TEST3 & TEST5 traces on ground referenced layer to the TPs

CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
133	0	0	1
166	0	1	1
200	0	1	0



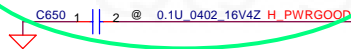
Intel :Don't Pull-up SiS : Pull-up 56ohm (Mount)

Intel :Pull-up 56ohm (Un-Mount) SiS : Pull-up 56ohm (Mount)

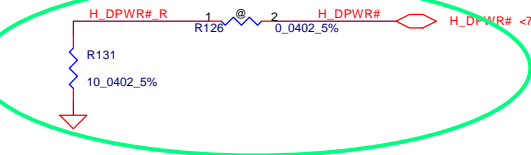
Intel :Don't Pull-up (Connecte to ICH) SiS : Pull-up 56ohm (Un-Mount)

Intel :Don't Pull-up SiS : Pull-up 56ohm (Un-Mount)

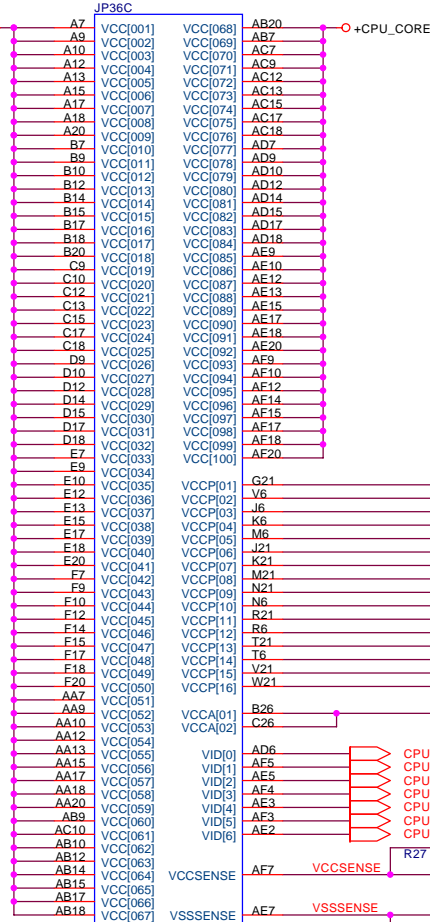
Intel :Don't Pull-down SiS :Pull-down Cap (Un-Mount)



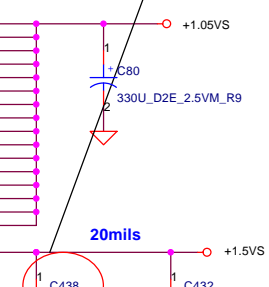
SiS Recommend



Resistor placed within 0.5" of CPU pin.Trace should be at least 25 mils away from any other toggling signal. COMP[0,2] trace width is 18 mils. COMP[1,3] trace width is 4 mils.



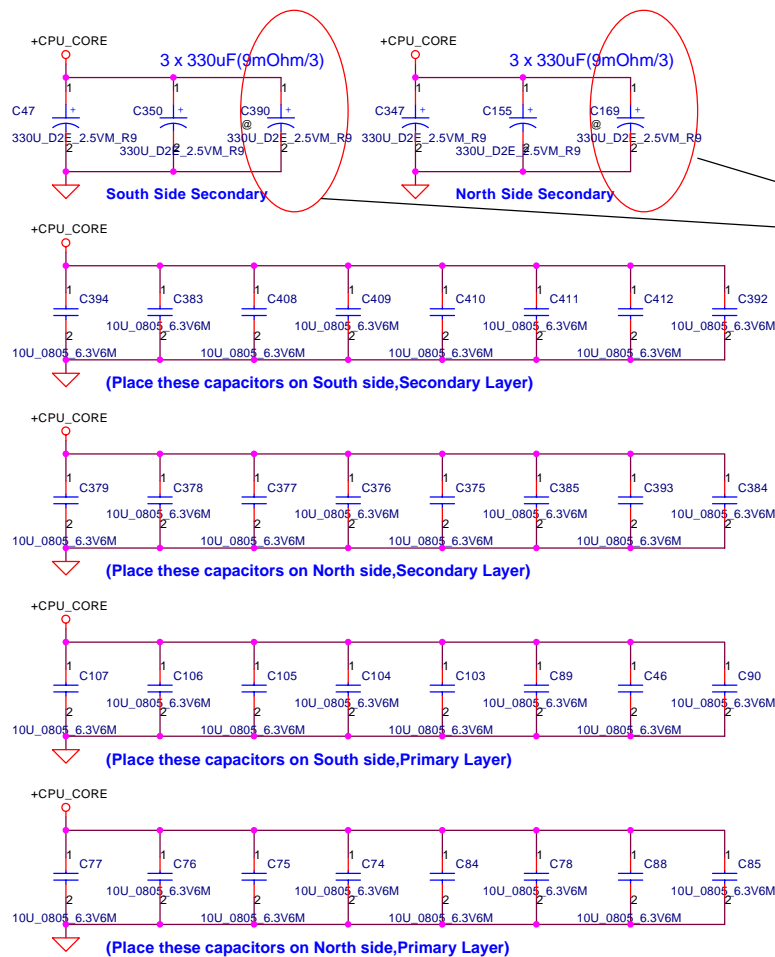
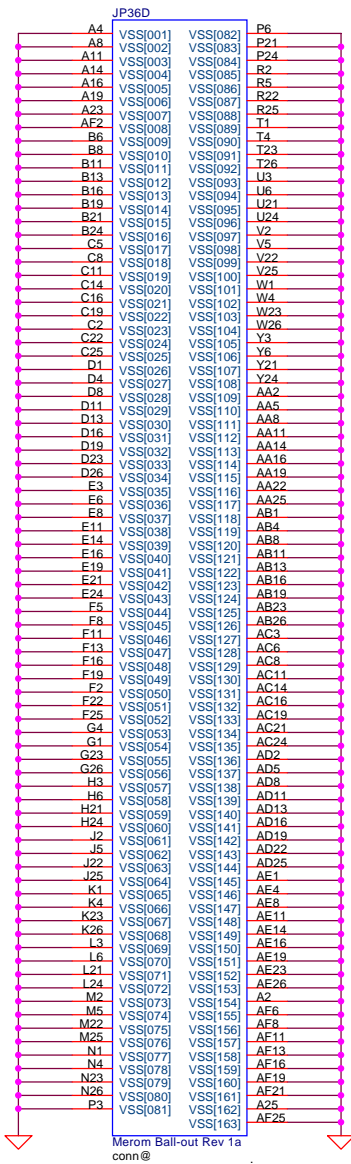
Place this cap more close to B26/C26 rather than 10UF



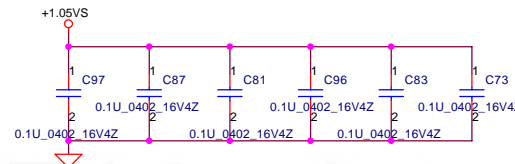
Length match within 25 mils. The trace width/space/other is 20/7/25.

Close to CPU pin within 500mils.

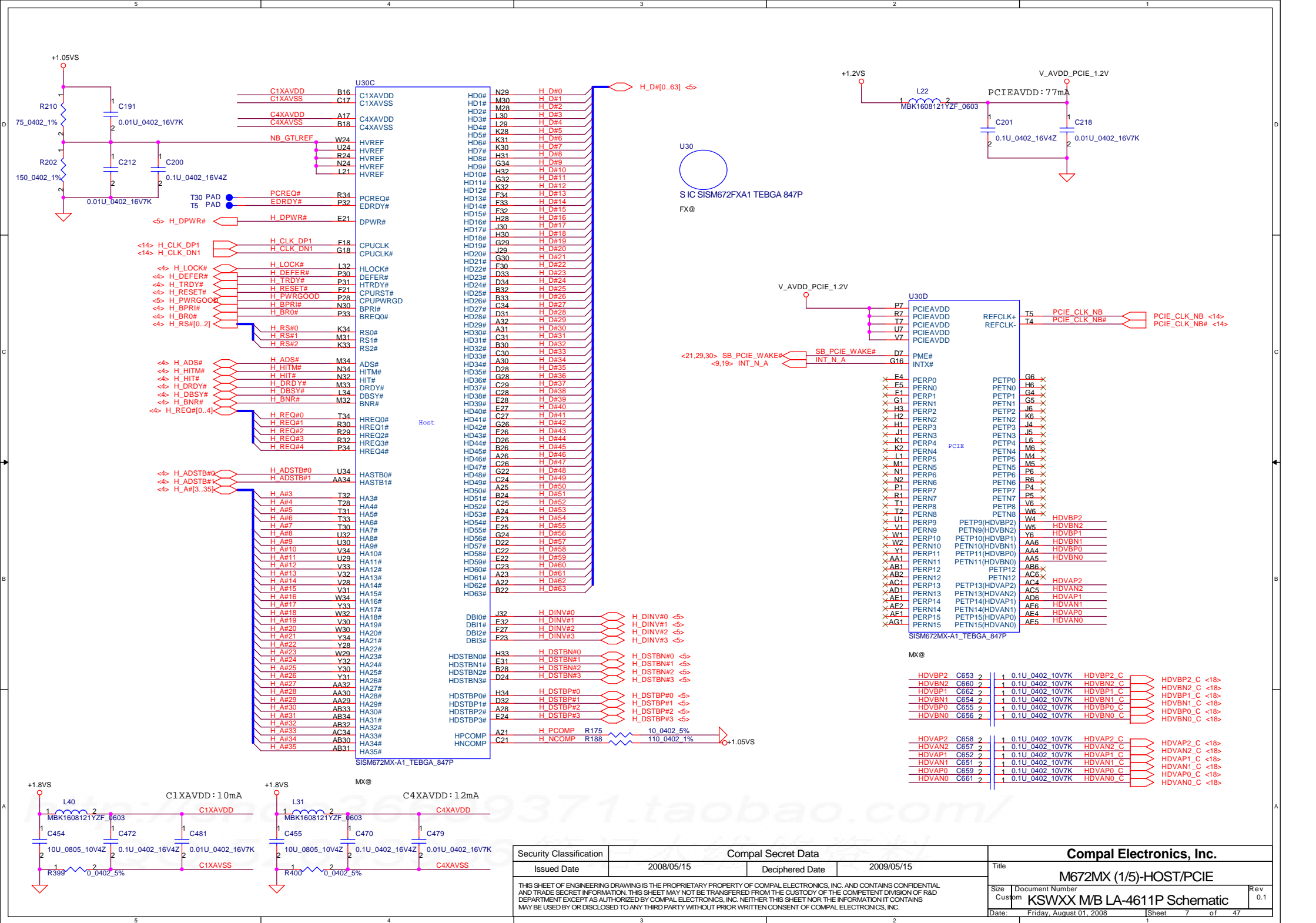
Security Classification		Compal Secret Data				Compal Electronics, Inc.									
Issued Date		2008/05/15		Deciphered Date		2009/05/15		Title							
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								Merom (2/3)							
								Size B	Document Number			KSWXX M/B LA-4611P Schematic			Rev 0.1
				Date:		Friday, August 01, 2008		Sheet		5 of 47					



+CPU-CORE Decoupling	C,uF	ESR, mohm	ESL,nH
SPCAP,Polymer	6X330uF	9m ohm/6	1.8nH/6
MLCC 0805 X5R	32X22uF	3m ohm/32	0.6nH/32
	32X10uF	3m ohm/32	0.6nH/32



Security Classification		Compal Secret Data		Compal Electronics, Inc.			
Issued Date		2008/05/15	Deciphered Date	2009/05/15		Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					Merom (3/3)		
					Size	Document Number	Rev
					B	KSWXX M/B LA-4611P Schematic	0.1
Date:		Friday, August 01, 2008		Sheet	6	of 47	



<12,13> DDRA_SDQ[0..63] DDRA_SDQ[0..63]
<12,13> DDRA_SDM[0..7] DDRA_SDM[0..7]
<12,13> DDRA_SMA[0..14] DDRA_SMA[0..14]

<12,13> DDRA_SQS0
<12,13> DDRA_SQS0#

<12,13> DDRA_SQS1
<12,13> DDRA_SQS1#

<12,13> DDRA_SQS2
<12,13> DDRA_SQS2#

<12,13> DDRA_SQS3
<12,13> DDRA_SQS3#

<12,13> DDRA_SQS4
<12,13> DDRA_SQS4#

<12,13> DDRA_SQS5
<12,13> DDRA_SQS5#

<12,13> DDRA_SQS6
<12,13> DDRA_SQS6#

<12,13> DDRA_SQS7
<12,13> DDRA_SQS7#

DDRA_SDQ0 AD31
DDRA_SDQ1 AD30
DDRA_SDQ2 AG34
DDRA_SDQ3 AE29
DDRA_SDQ4 AE32
DDRA_SDQ5 AF34
DDRA_SDQ6 AF31
DDRA_SDQ7 AE30
DDRA_SDM0 AD28
DDRA_SQS0 AF32
DDRA_SQS0# AF33
DDRA_SDQ8 AF28
DDRA_SDQ9 AJ34
DDRA_SDQ10 AH31
DDRA_SDQ11 AG30
DDRA_SDQ12 AF30
DDRA_SDQ13 AG32
DDRA_SDQ14 AJ32
DDRA_SDQ15 AH31
DDRA_SDM1 AH34
DDRA_SQS1 AH32
DDRA_SQS1# AH33
DDRA_SDQ16 AK34
DDRA_SDQ17 AH30
DDRA_SDQ18 AL32
DDRA_SDQ19 AM33
DDRA_SDQ20 AK32
DDRA_SDQ21 AG29
DDRA_SDQ22 AD34
DDRA_SDQ23 AJ30
DDRA_SDM2 AJ30
DDRA_SQS2 AK33
DDRA_SQS2# AL34
DDRA_SDQ24 AM32
DDRA_SDQ25 AP32
DDRA_SDQ26 AP31
DDRA_SDQ27 AM29
DDRA_SDQ28 AK30
DDRA_SDQ29 AK29
DDRA_SDQ30 AJ27
DDRA_SDQ31 AK28
DDRA_SDM3 AN32
DDRA_SQS3 AM30
DDRA_SQS3# AM31
DDRA_SDQ32 AK20
DDRA_SDQ33 AM20
DDRA_SDQ34 AM19
DDRA_SDQ35 AJ19
DDRA_SDQ36 AN20
DDRA_SDQ37 AJ21
DDRA_SDQ38 AP19
DDRA_SDQ39 AH20
DDRA_SDM4 AK21
DDRA_SQS4 AK19
DDRA_SQS4# AL19
DDRA_SDQ40 AK18
DDRA_SDQ41 AJ17
DDRA_SDQ42 AK17
DDRA_SDQ43 AP16
DDRA_SDQ44 AH18
DDRA_SDQ45 AP18
DDRA_SDQ46 AN18
DDRA_SDQ47 AP17
DDRA_SDM5 AM18
DDRA_SQS5 AL17
DDRA_SQS5# AM17
DDRA_SDQ48 AN16
DDRA_SDQ49 AK16
DDRA_SDQ50 AN14
DDRA_SDQ51 AJ15
DDRA_SDQ52 AM15
DDRA_SDQ53 AM16
DDRA_SDQ54 AK15
DDRA_SDQ55 AP14
DDRA_SDM6 AH16
DDRA_SQS6 AL15
DDRA_SQS6# AM15
DDRA_SDQ56 AL13
DDRA_SDQ57 AM13
DDRA_SDQ58 AM12
DDRA_SDQ59 AJ13
DDRA_SDQ60 AM14
DDRA_SDQ61 AK14
DDRA_SDQ62 AN12
DDRA_SDQ63 AH14
DDRA_SDM7 AK13
DDRA_SQS7 AP12
DDRA_SQS7# AP13

U30B

DRAM

SISM672MX-A1_TEBGA_847P

MX@

D1XAVDD
D1XAVSS
D4XAVDD
D4XAVSS

MA0A
MA1A
MA2A
MA3A
MA4A
MA5A
MA6A
MA7A
MA8A
MA9A
MA10A
MA11A
MA12A
MA13A
MA14A
MA15A
MA16A
MA17A

RASA#
CASA#
WEA#

FWSDCLKOA
FWSDCLKOA#

CS0A#
CS1A#
CS2A#
CS3A#

ODT0A
ODT1A
ODT2A
ODT3A

CKEA0
CKEA1
CKEA2
CKEA3

DDRVREF0
DDRVREF1

DDRCOMP
DDRCOMN

OCDVREFP
OCDVREFN

S3AUXSW#

A15 D1XAVDD
B15 D1XAVSS
AP11 D4XAVDD
AP10 D4XAVSS

AH24 DDRA_SMA0
AP25 DDRA_SMA1
AM25 DDRA_SMA2
AL25 DDRA_SMA3
AP26 DDRA_SMA4
AM26 DDRA_SMA5
AK25 DDRA_SMA6
AP27 DDRA_SMA7
AP28 DDRA_SMA8
AK24 DDRA_SMA9
AN24 DDRA_SBS0
AP24 DDRA_SBS1
AM28 DDRA_SBS2
AN27 DDRA_SMA11
AN28 DDRA_SMA12
AP21 DDRA_SMA13
AP29 DDRA_SMA14

AM23 DDRA_SRAS#
AP22 DDRA_SCAS#
AJ23 DDRA_SWE#

AK12 CLK_INT
AH12 CLK_INC

AP23 DDRA_SCS0#
AH22 DDRA_SCS1#
AM22 DDRA_SCS2#
AM21 DDRA_SCS3#

AK22 DDRA_ODT0
AP20 DDRA_ODT1
AN22 DDRA_ODT2
AL21 DDRA_ODT3

AN30 DDRA_CKE0
AP30 DDRA_CKE1
AH26 DDRA_CKE2
AK27 DDRA_CKE3

AD18 DDRVREF
AD23

AJ25 DDRCOMP
AK26 DDRCOMN

AH28 OCDVREFP
AJ29 OCDVREFN

B6 S3AUXSW#

DDRA_SBS0 <12,13>
DDRA_SBS1 <12,13>
DDRA_SBS2 <12,13>

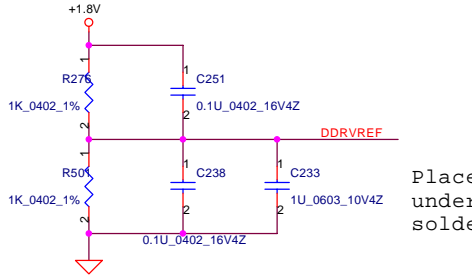
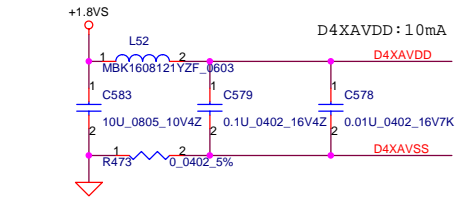
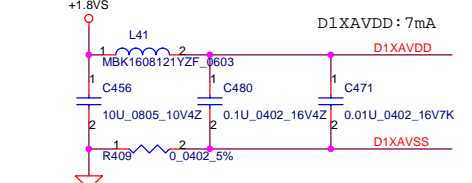
DDRA_SRAS# <12,13>
DDRA_SCAS# <12,13>
DDRA_SWE# <12,13>

CLK_INT <15>
CLK_INC <15>

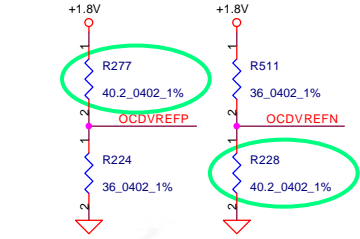
DDRA_SCS0# <12>
DDRA_SCS1# <12>
DDRA_SCS2# <13>
DDRA_SCS3# <13>

DDRA_ODT0 <12>
DDRA_ODT1 <12>
DDRA_ODT2 <13>
DDRA_ODT3 <13>

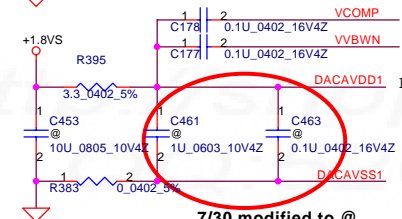
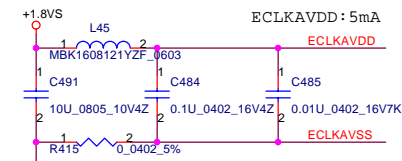
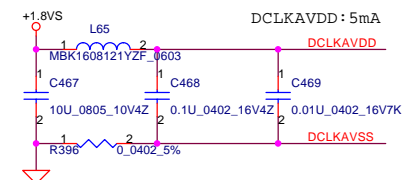
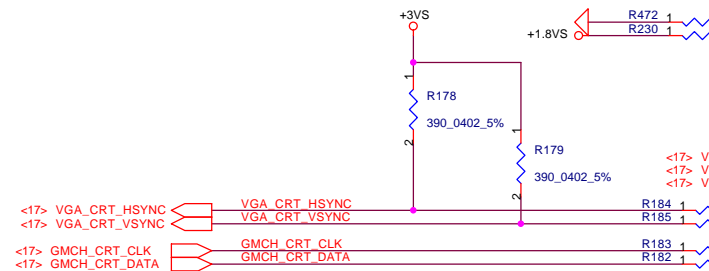
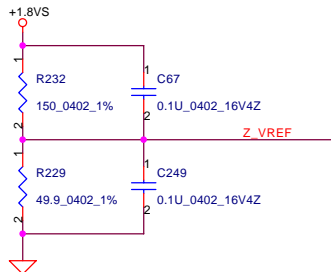
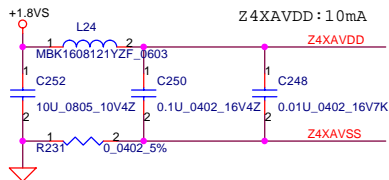
DDRA_CKE0 <12>
DDRA_CKE1 <12>
DDRA_CKE2 <13>
DDRA_CKE3 <13>



Place C233
under M672MX
solder side.

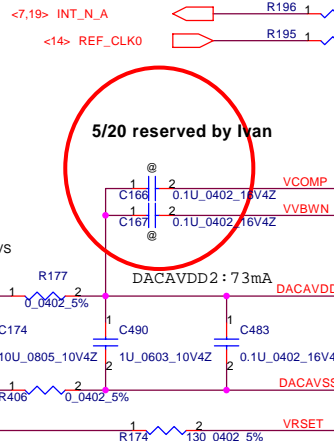


Security Classification		Compal Secret Data				Compal Electronics, Inc.			
Issued Date		2006/08/18		Deciphered Date		2007/8/18		Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		M672MX (2/5)-DDR							
		Size	Document Number	Rev					
		Custom	KSWXX M/B LA-4611P Schematic	0.1					
		Date:	Friday, August 01, 2008	Sheet	8	of	47		
3				2					

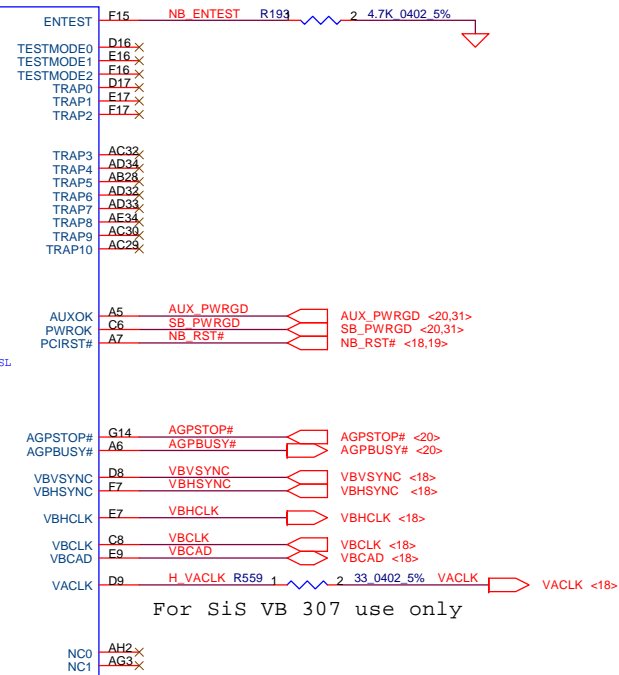
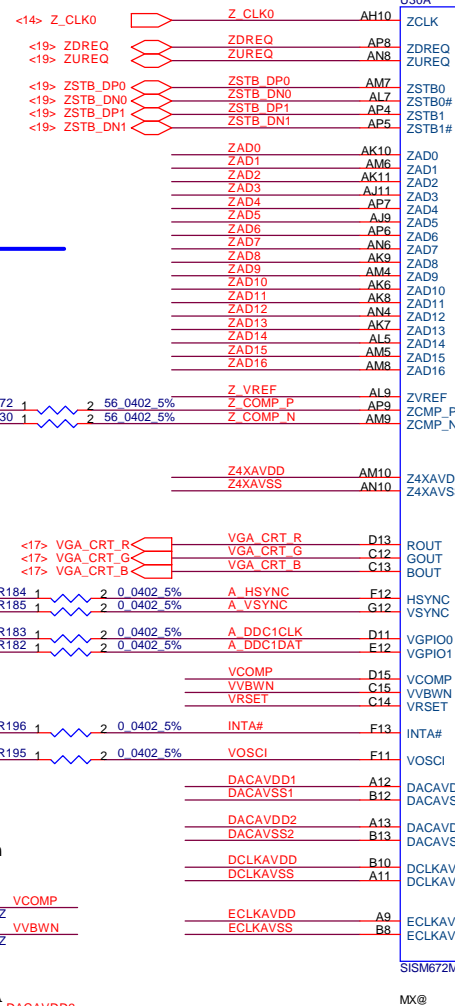


DACAVDD1 Spec.
Voltage : 1.5V +/- 5%
Current : 100mA

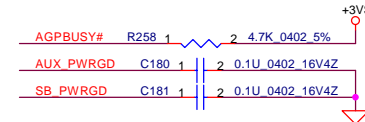
730 modified to @



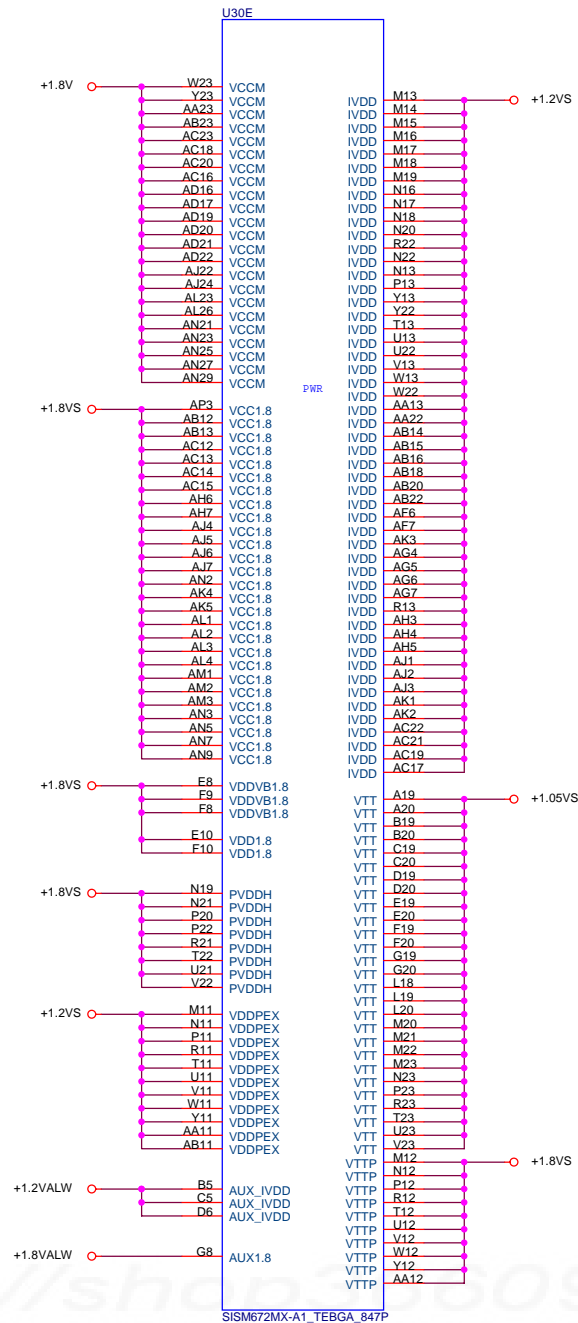
5/20 reserved by Ivan



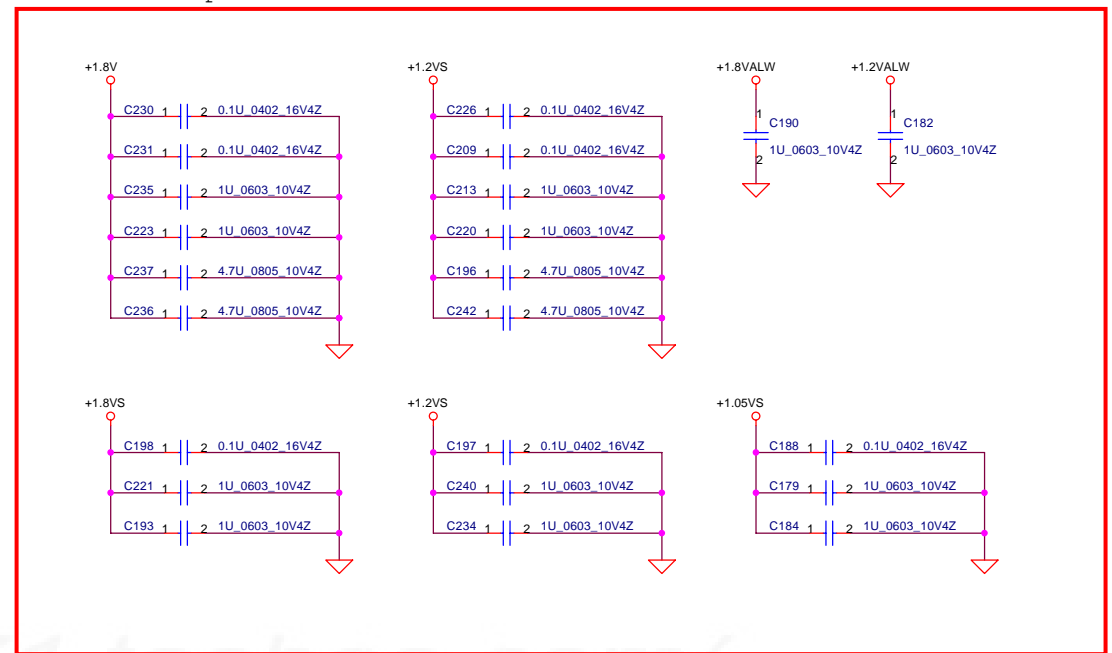
For SiS VB 307 use only



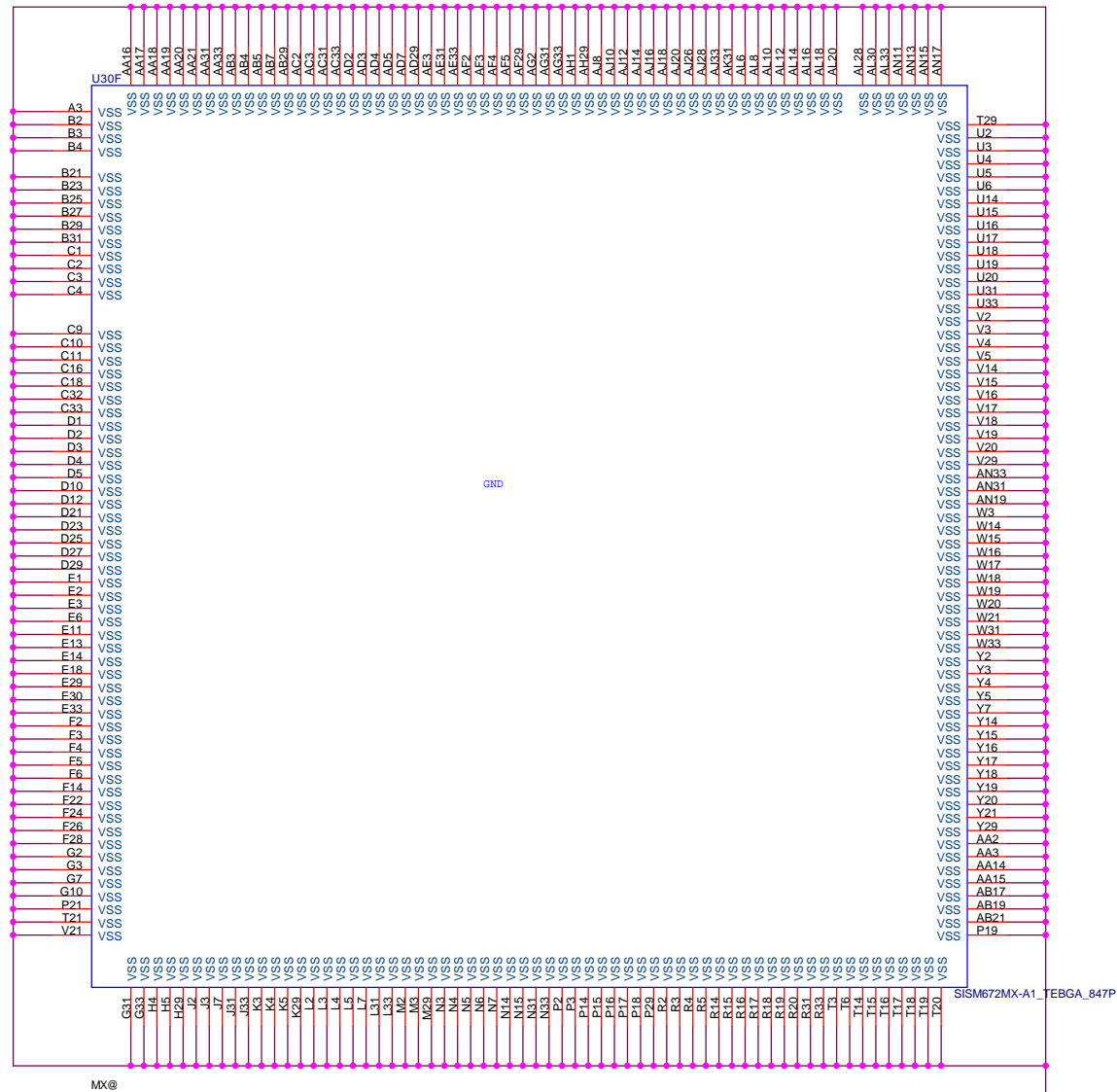
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title	M672MX (3/5)-ASL
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number KSWXX M/B LA-4611P Schematic
				Date: Friday, August 01, 2008	Sheet 9 of 47



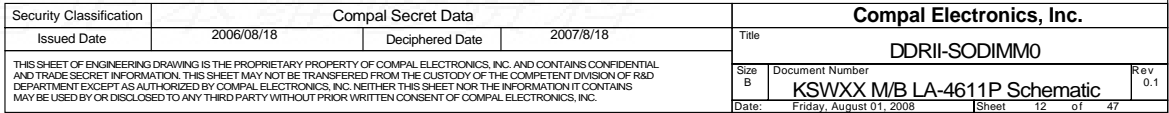
Place these Cap under M672MX solder side.

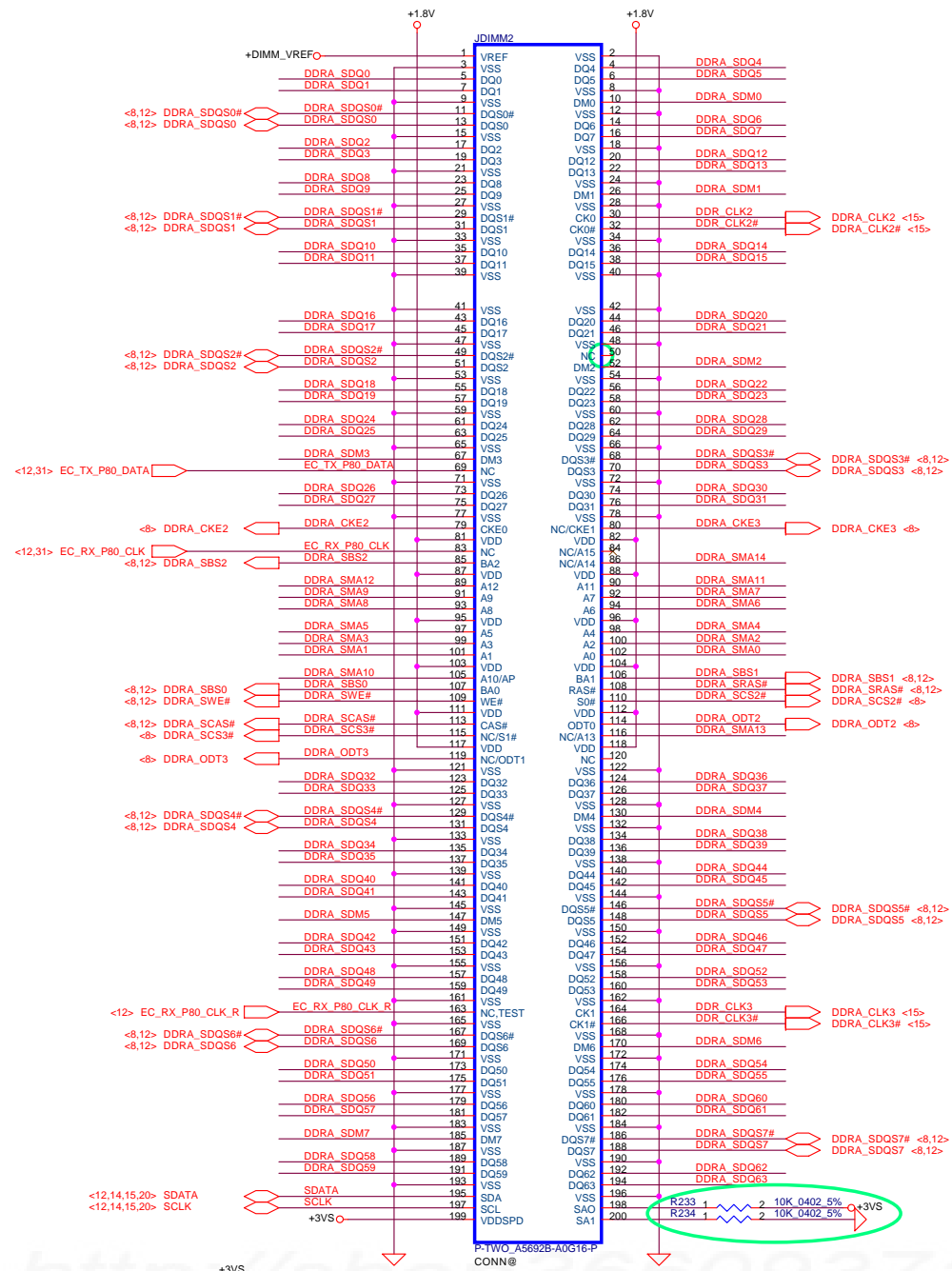


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				M672MX (4/5)-POWER	
Size	Document Number	Rev		0.1	
Custom	KSWXX M/B LA-4611P Schematic				
Date:	Friday, August 01, 2008	Sheet	10	of	47

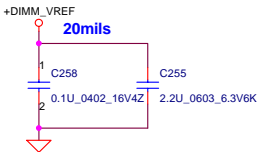


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				M672MX (5/5)-GND	
Size	Document Number	Rev		0.1	
Custom	KSWXX M/B LA-4611P Schematic				
Date:	Friday, August 01, 2008	Sheet	11	of	47



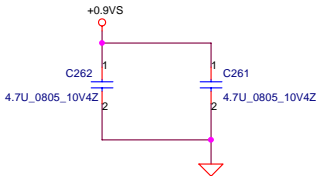
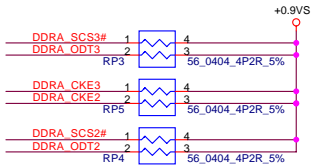
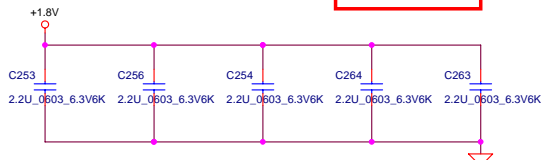


DIMM1 STD H:9.2mm (BOT)



<8,12> DDRA_SMA[0..14] DDRA_SMA[0..14]
<8,12> DDRA_SDQ[0..63] DDRA_SDQ[0..63]
<8,12> DDRA_SDM[0..7] DDRA_SDM[0..7]

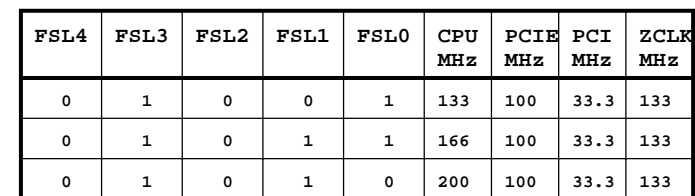
Layout Note:
Place near JP34



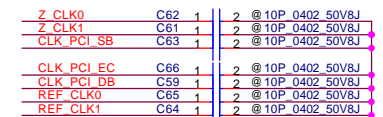
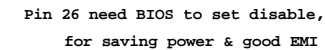
Layout Note:
Place these resistor
closely JP34,all
trace length Max=1.5"

Layout Note:
Place one cap close to every 2 pullup
resistors terminated to +0.9VS

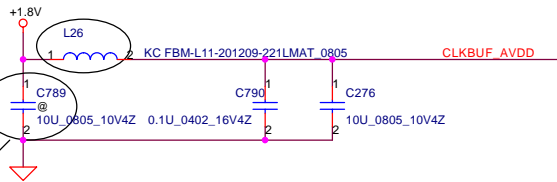
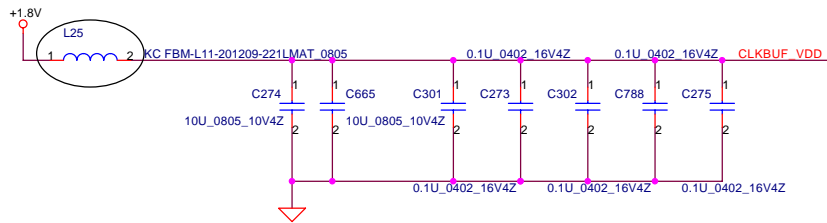
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				DDR2-SODIMM1	
Size	B	Document Number	JSWXX M/B LA-4611P Schematic	Rev	0.1
Date:	Friday, August 01, 2008	Sheet	13	of	47



Remove R43 R66 R529 R527 and use PR39
Michael 2008/5/30

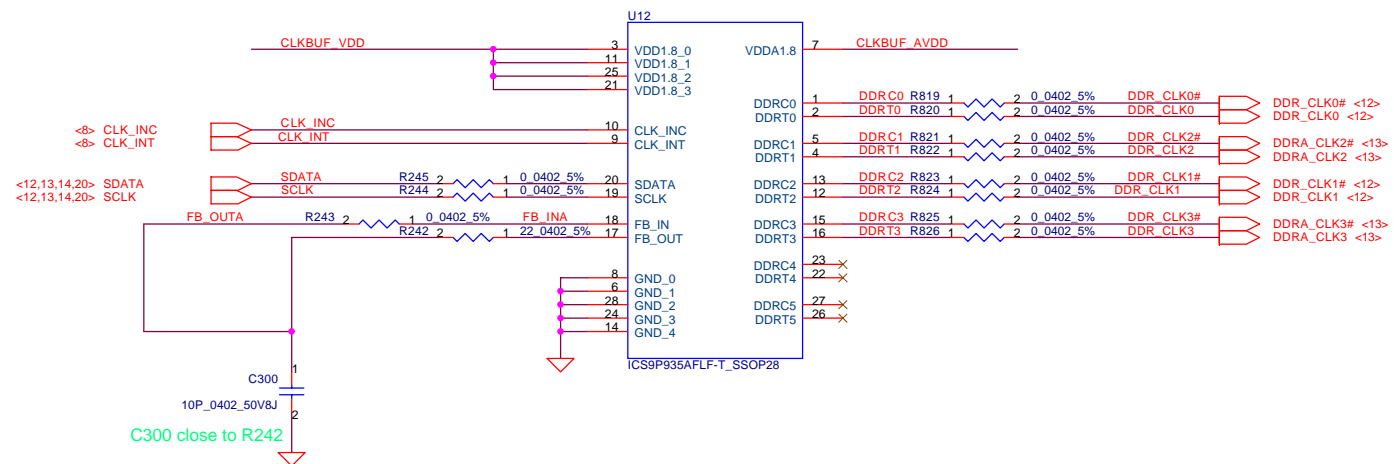


Security Classification		Compal Secret Data				
Issued Date	2005/03/01	Deciphered Date	2006/03/01	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Clock Generator ICS9LPR600C		
				Size Custom	Document Number	Rev
				KSWXX M/B LA-4611P Schematic		0.1
Date:		Friday, August 01, 2008	Sheet	14	of 47	



Reference schematic use 1206

Horizontal rotate

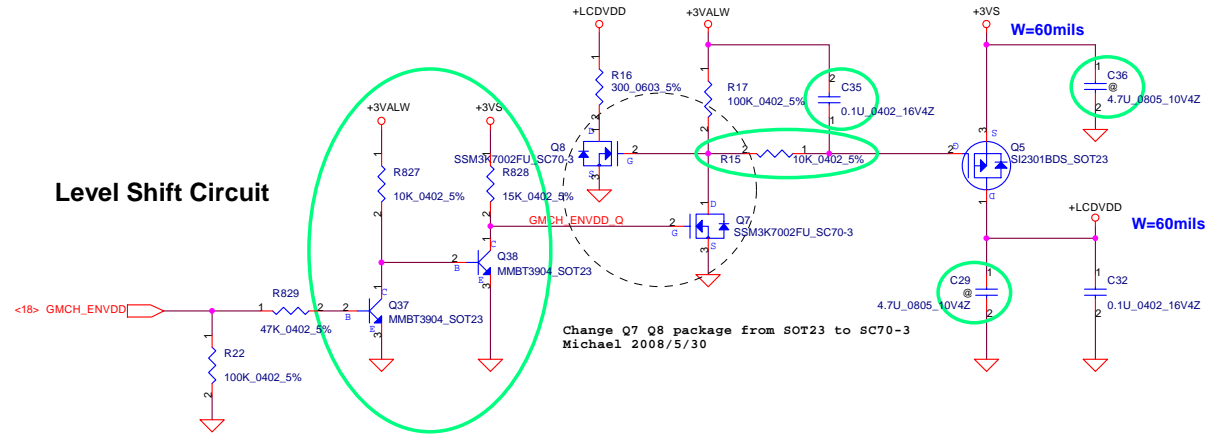


C300 close to R242

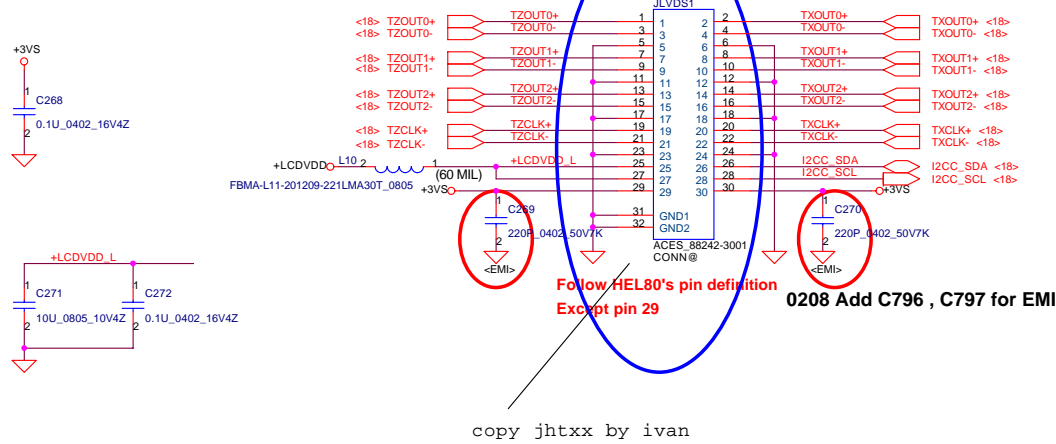
<http://shop36609371.taobao.com/>
QQ:52643956

Security Classification		Compal Secret Data		Title	
Issued Date	2005/03/01	Deciphered Date	2006/03/01	Clock Buffer ICS9P935	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number KSWXX M/B LA-4611P Schematic
				Date: Friday, August 01, 2008	Rev 0.1
				Sheet 15 of 47	

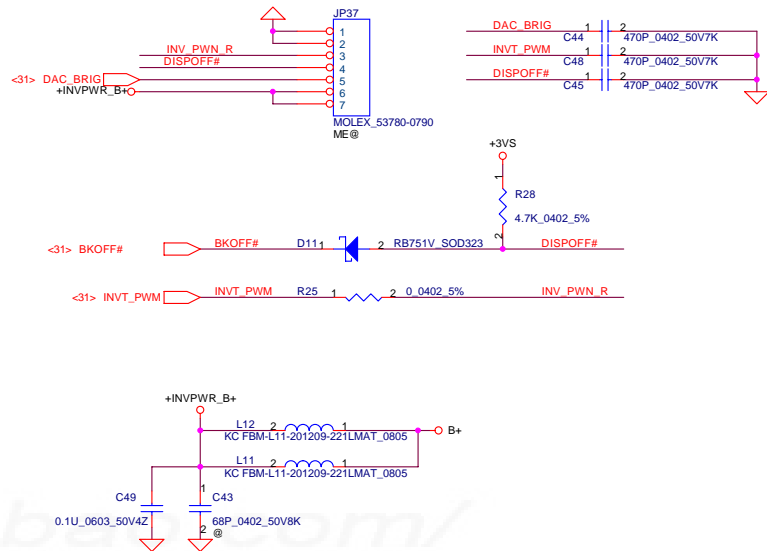
LCD POWER CIRCUIT



LCD/PANEL BD. Conn. (IFTXX)



INVERTER Conn.



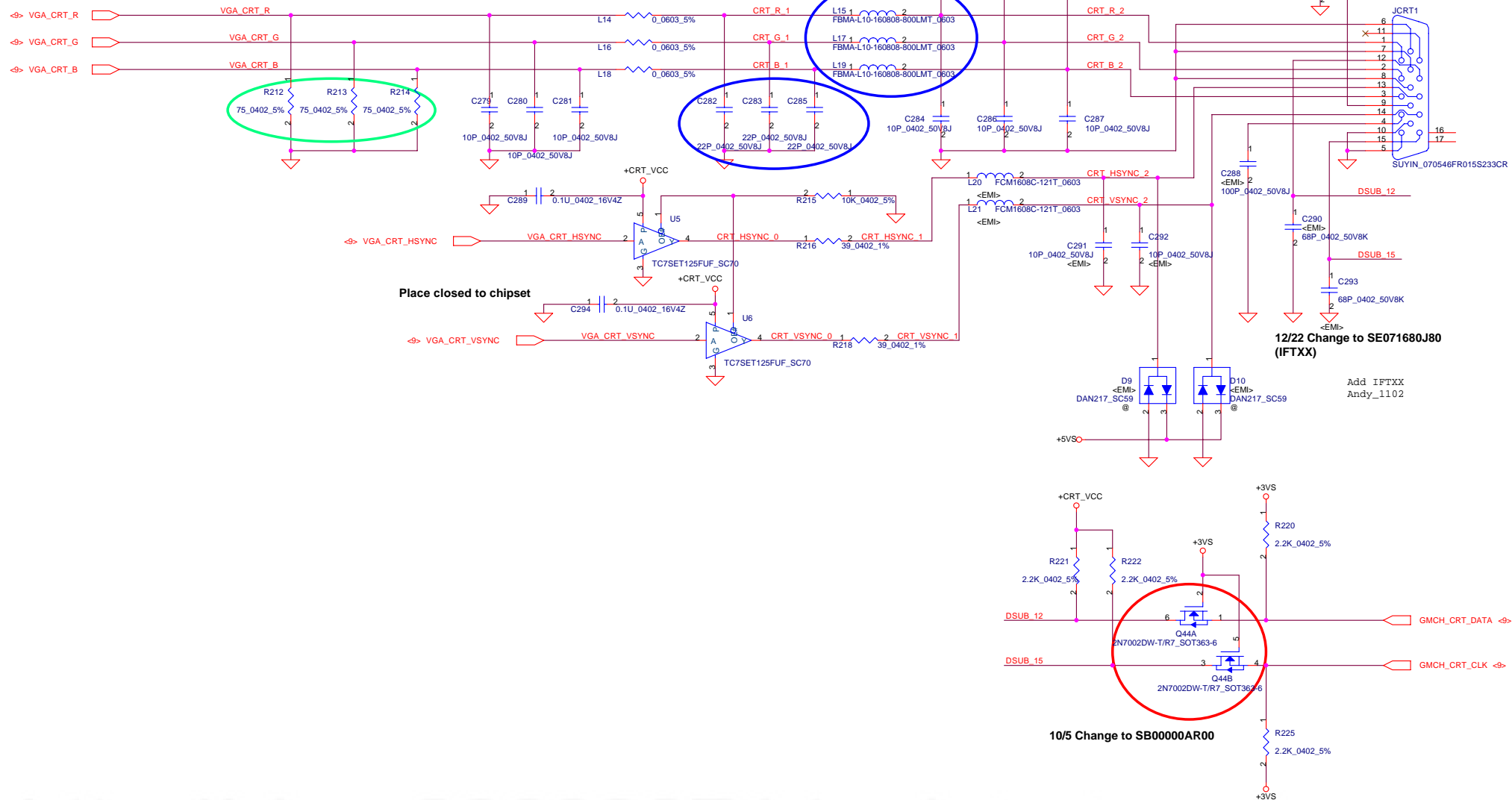
Security Classification		Compal Secret Data		Compal Electronics, Inc.			
Issued Date		2006/08/18	Deciphered Date	2007/8/18	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					LVDS & DVI Connector		
					Size B	Document Number	Rev 0.1
					KSWXX M/B LA-4611P Schematic		
Date:		Friday, August 01, 2008		Sheet 16	of 47		

CRT Connector

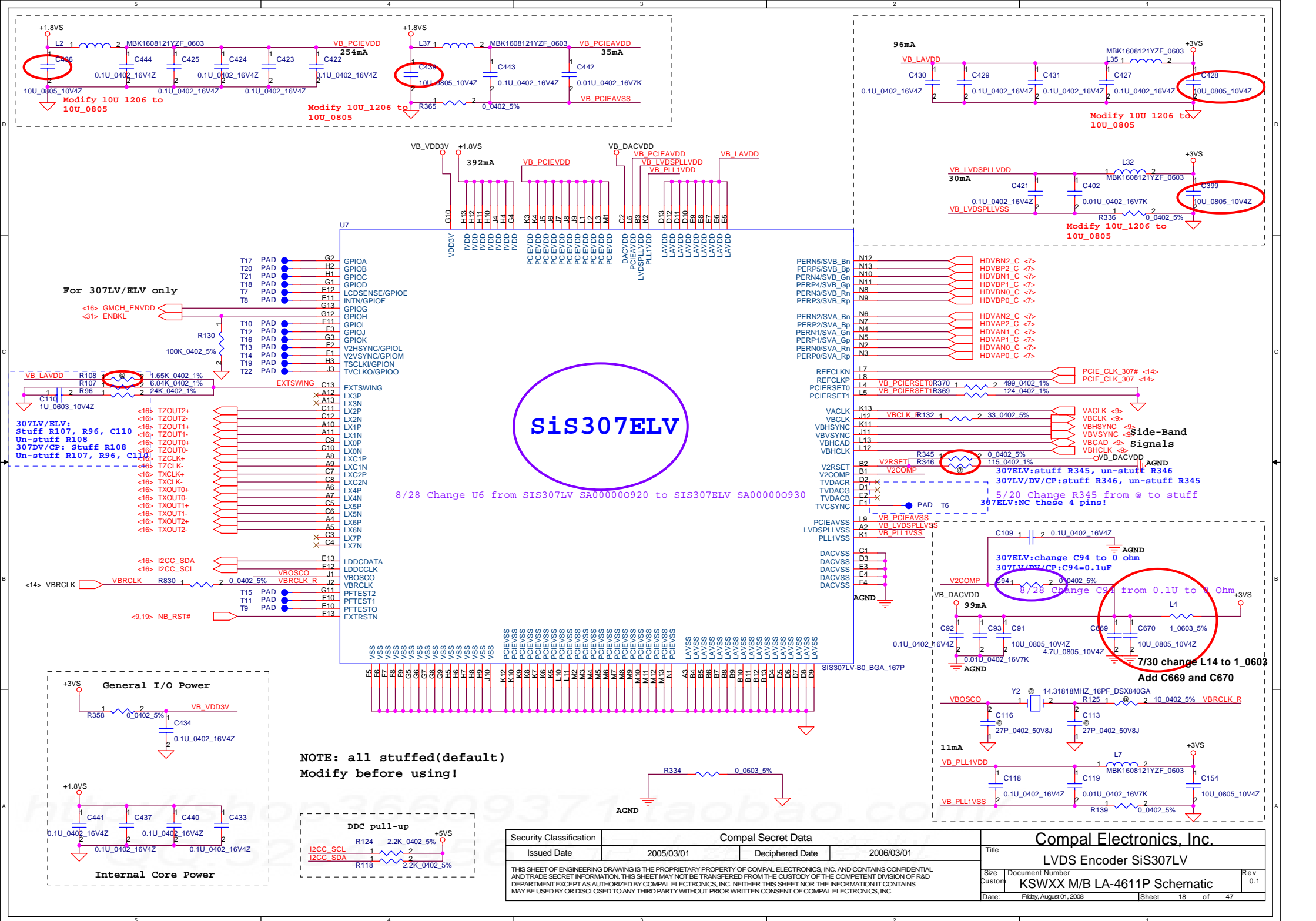
Checklist recommend: 2-pole filter on R/G/B signals
C - L - C - L - C
10p - 47 Ohm/100MHz - 22p - 47 Ohm/100MHz - 10p

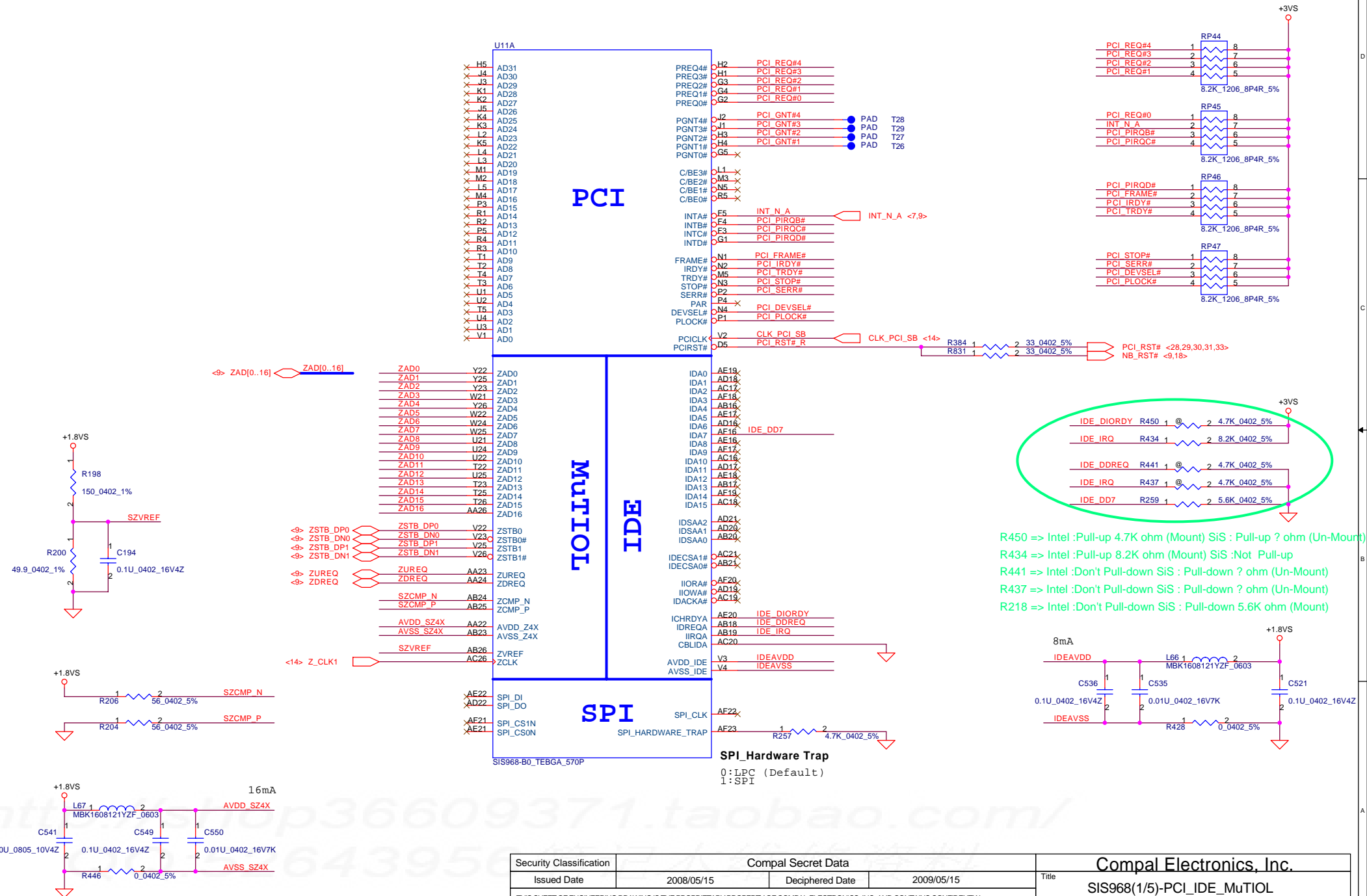
Place closed to chipset

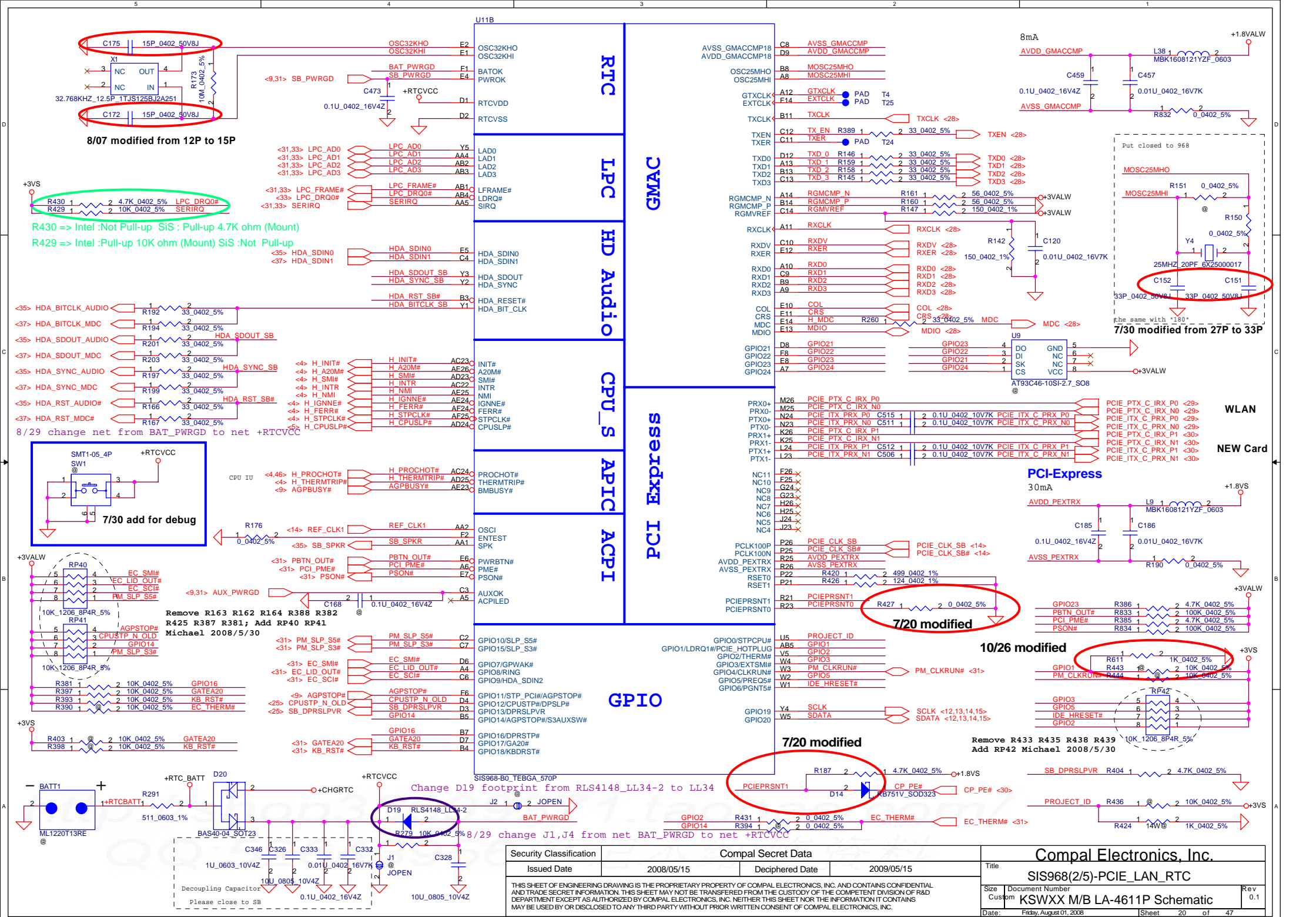
12/15 Modified. Note L26~L30 are 0 Ohm resistors (IFTXX)

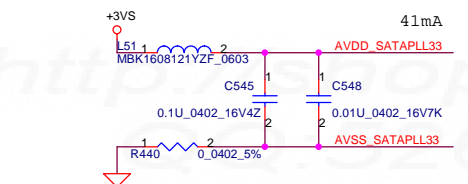
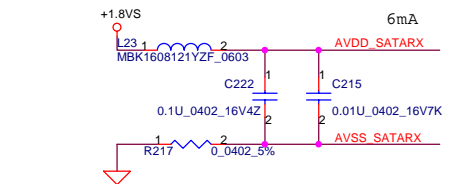
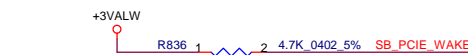
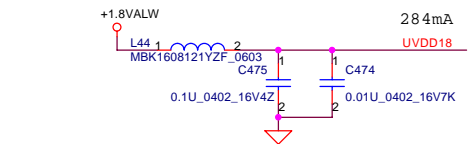
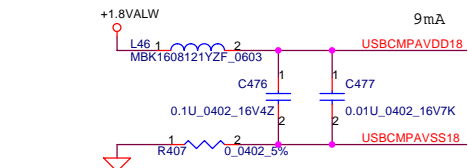
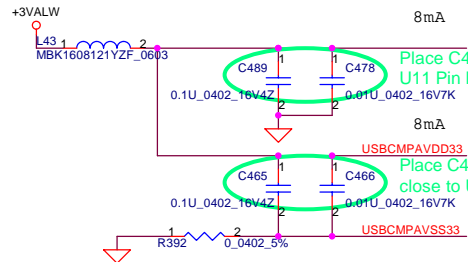
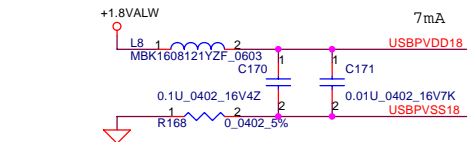


Security Classification			Compal Secret Data		Title	
Issued Date	2006/08/18	Deciphered Date	2007/8/18		Size B	Document Number
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Date:	Friday, August 01, 2008	Sheet 17 of 47
				Compal Electronics, Inc.		
				CRT & TV-OUT Connector		
				KSWXX M/B LA-4611P Schematic		
				Rev 0.1		









New Card
WLAN
USB
USB
Card Reader
CAMERA

USB
BT
WLAN
USB
USB
Card Reader
CAMERA

USB OC#06
USB OC#45
USB OC#6
USB OC#7

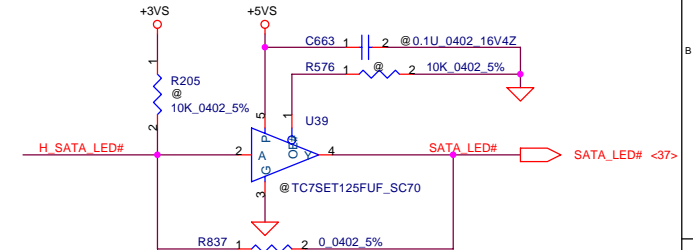
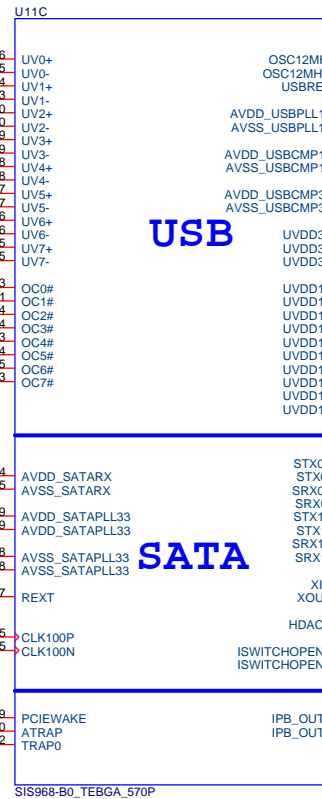
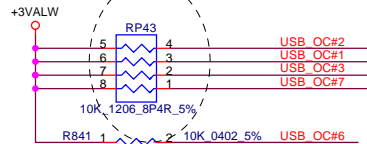
AVDD_SATARX
AVSS_SATARX
AVDD_SATAPLL33
AVSS_SATAPLL33
AVSS_SATAPLL33
AVSS_SATAPLL33

SATA REXT
SATA CLK_DP
SATA CLK_DN

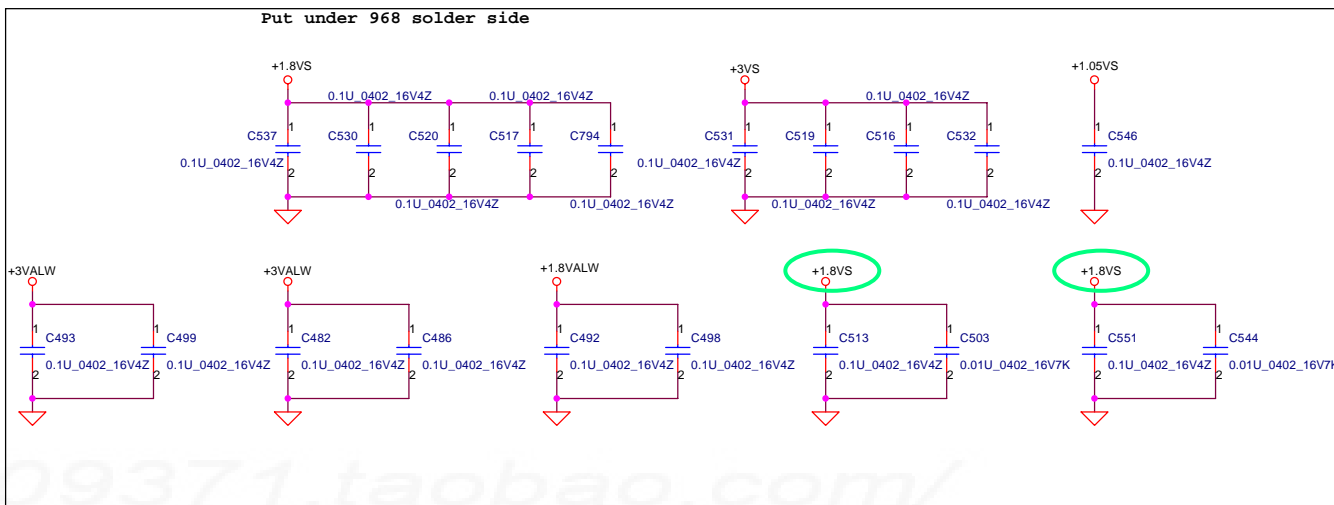
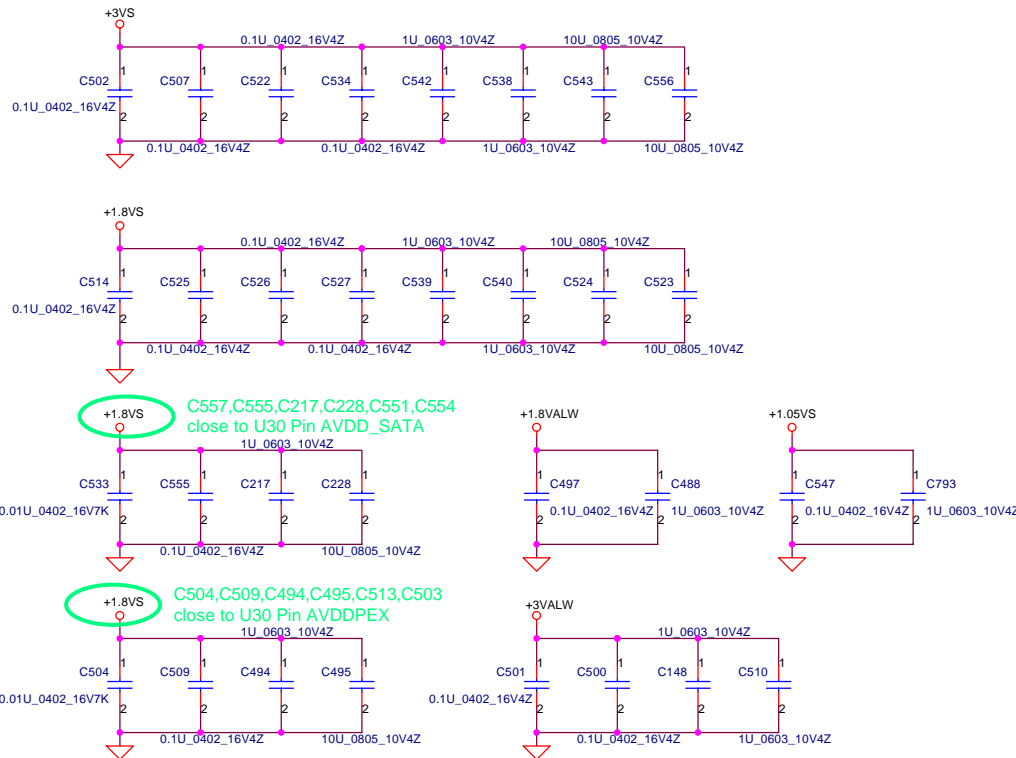
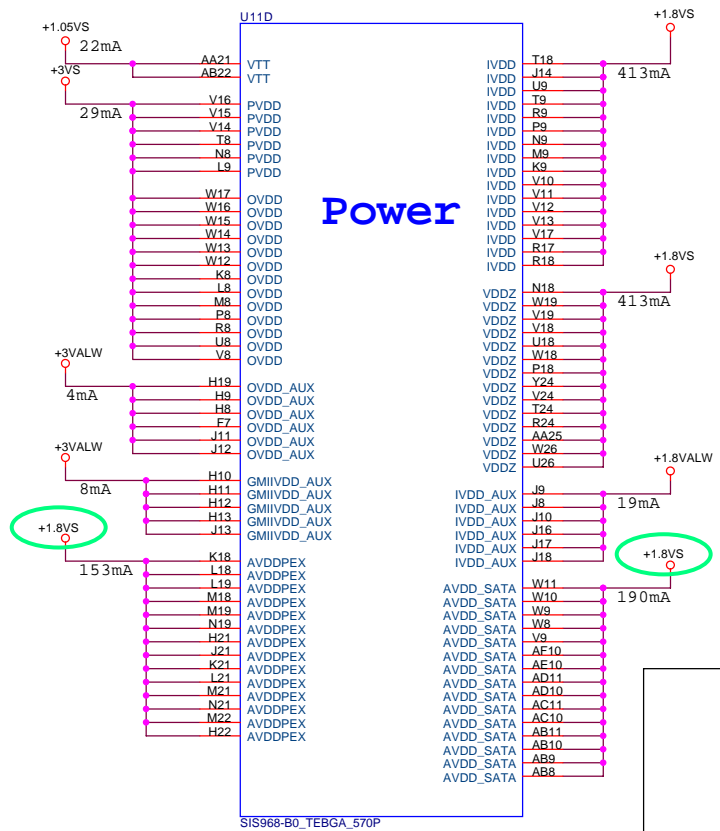
SB_PCIE_WAKE#
ATRAPP
TRAPP

133MHz:Internal pull-down
66MHz:External pull-up

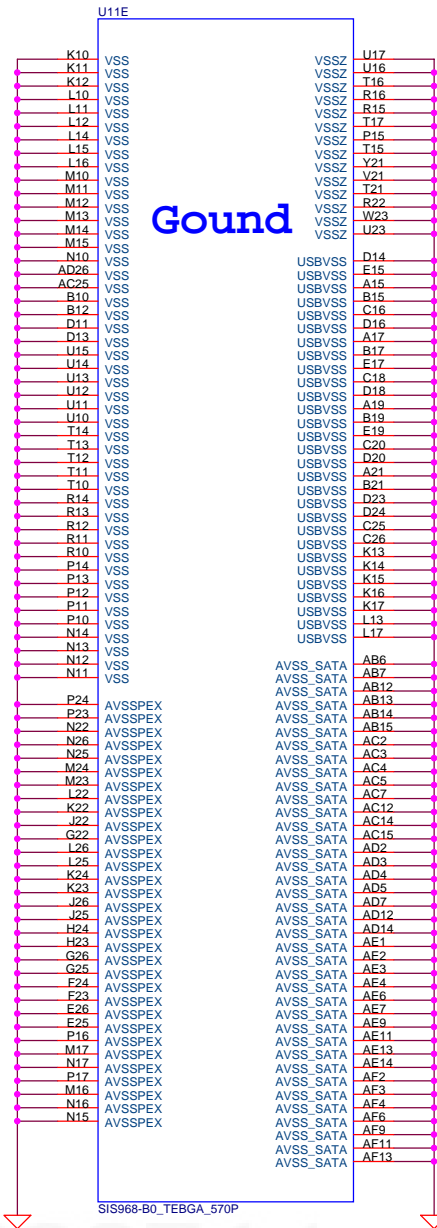
Remove R838 R839 R840 R552
Add RP43 Michael 2008/5/30



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/15	Deciphered Date	2009/05/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SIS968(3/5)-USB_SATA	
Size	Custom	Document Number	KSWXX M/B LA-3961P Schematic	Rev	0.1
Date:	Friday, August 01, 2008	Sheet	21	of	47

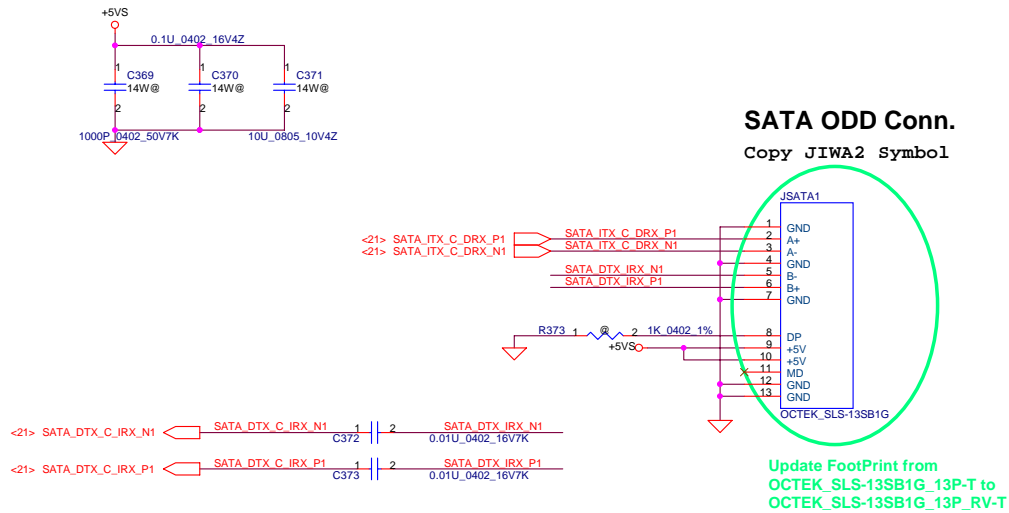


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/15	Deciphered Date	2009/05/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SIS968(4/5)-POWER	
Size	Custom	Document Number	KSWXX M/B LA-4611P Schematic		Rev 0.1
Date:	Friday, August 01, 2008	Sheet	22	of	47

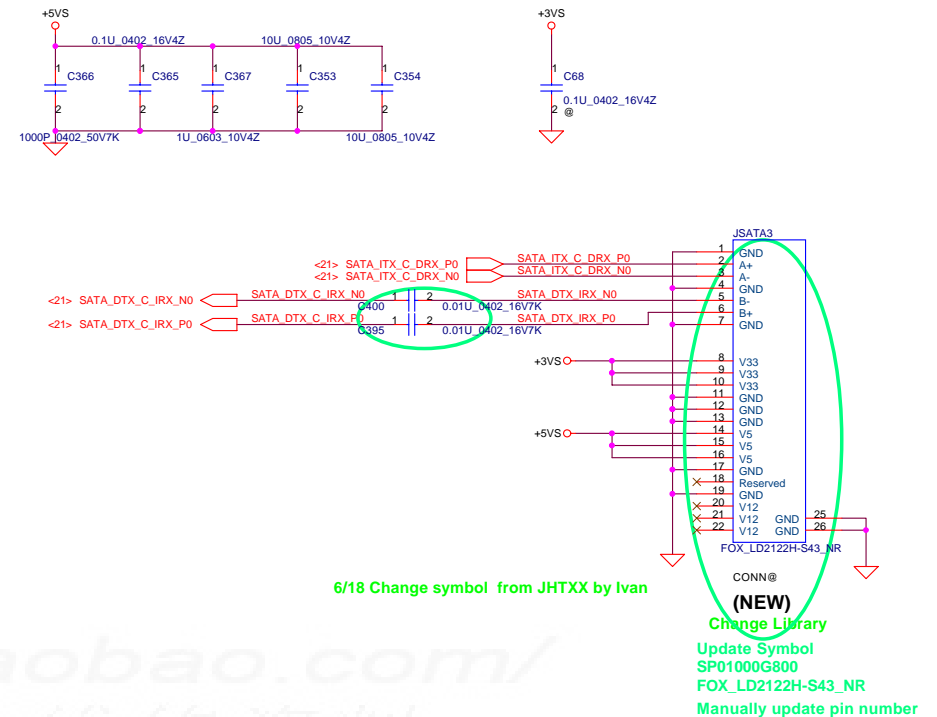


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/15	Deciphered Date	2009/05/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SIS968(5/5)-GND	
Size		Document Number		Rev	
Custom		KSWXX M/B LA-4611P Schematic		0.1	
Date:		Friday, August 01, 2008		Sheet 23 of 47	

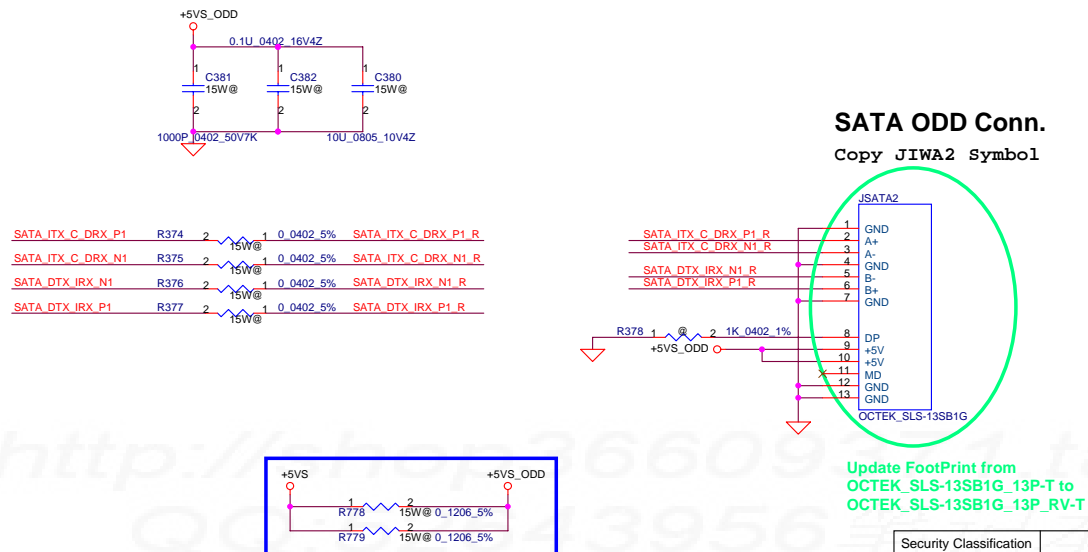
14W SATA ODD Conn.



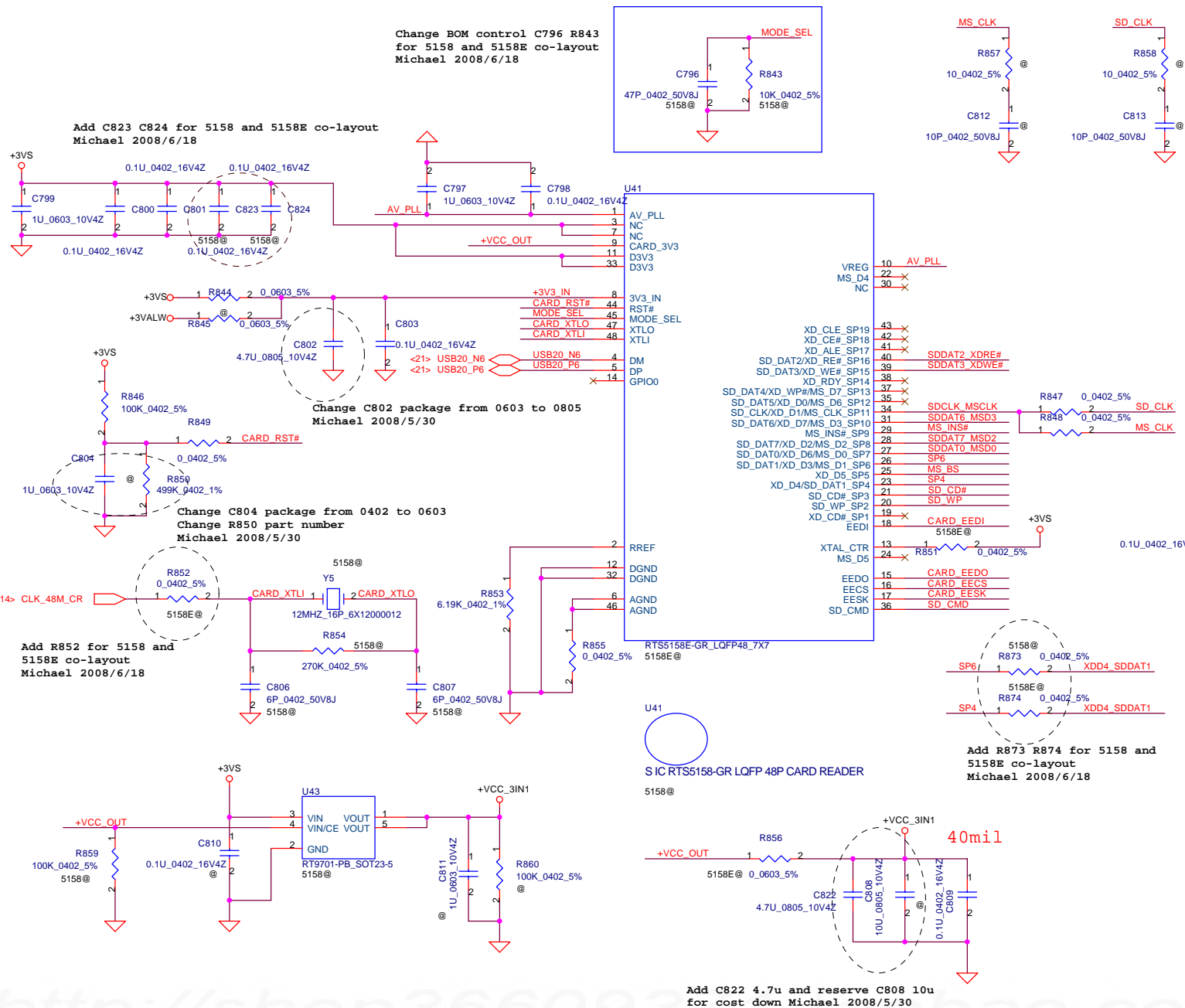
SATA HDD Conn.



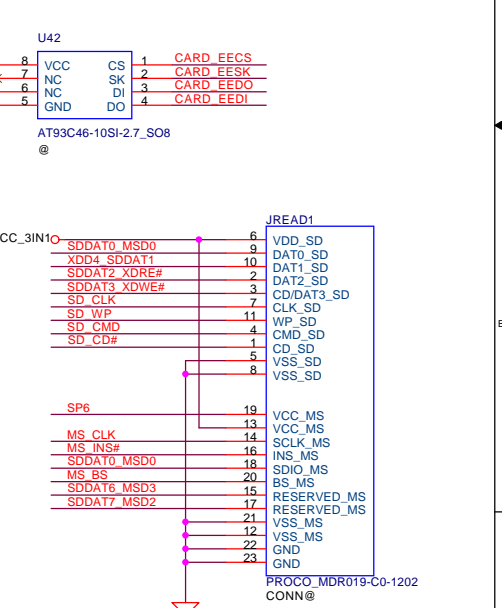
15W SATA ODD Conn.



Security Classification		Compal Secret Data		Title	
Issued Date	2008/05/15	Deciphered Date	2009/05/15	HDD & ODD Connector	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size B	Document Number
				KSWXX M/B LA-4611P Schematic	
				Date: Friday, August 01, 2008	Sheet 24 of 47
				Rev 0.1	

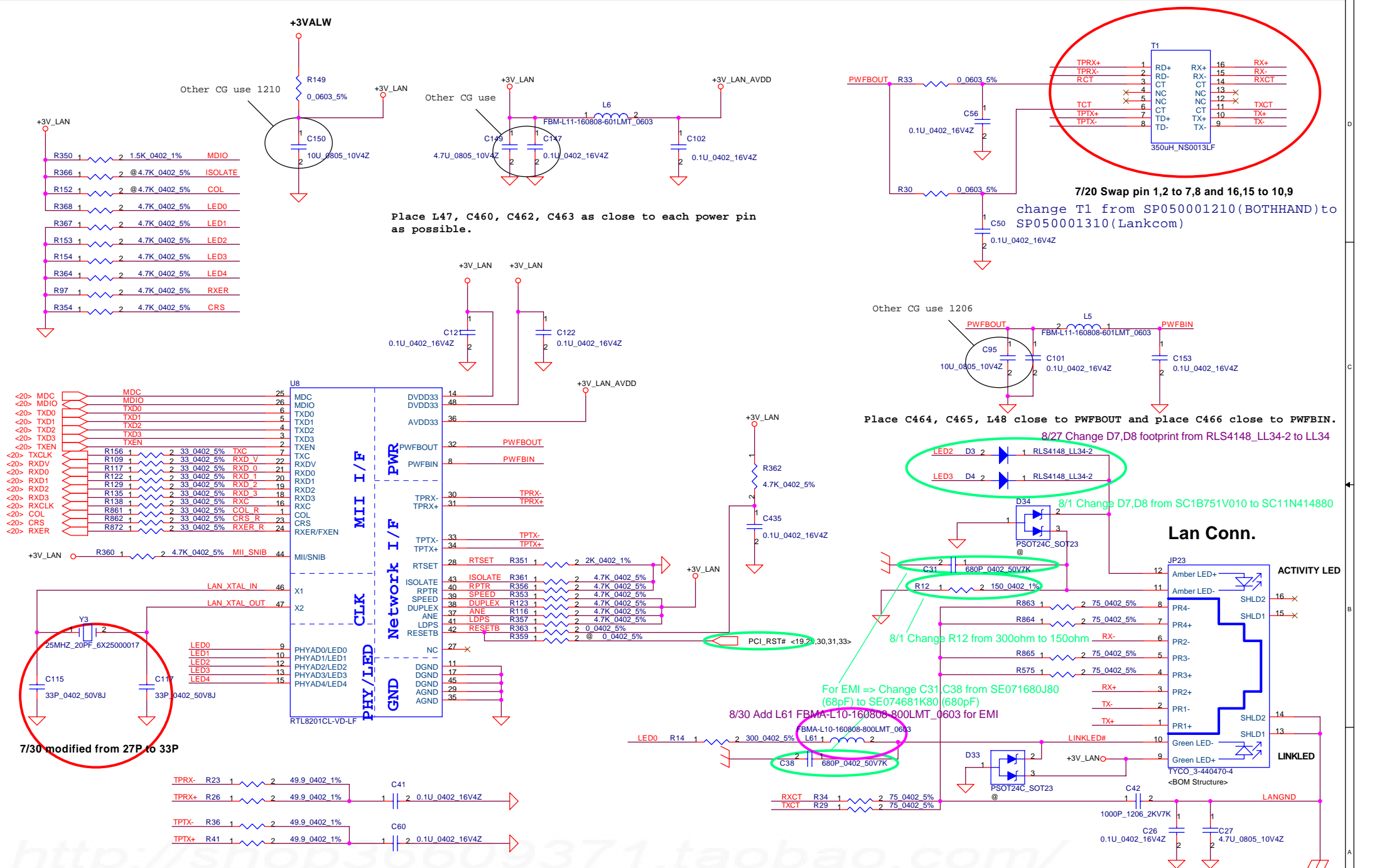


SD,MMC,MS muti-function pin define			
MDIO PIN Name	SD Card PIN Name	MMC Card PIN Name	MS Card PIN Name
SP1			
SP2	SDWP#		
SP3	SDCD#		
SP4	SDCDAT1		MSWR
SP5			MSBS
SP6	SDCDAT1		MSCDAT1
SP7	SDCDAT0		MSCDAT0
SP8	SDCDAT7		MSCDAT2
SP9			MS_INS#
SP10	SDCDAT6		MSCDAT3
SP11	SDCCLK		MSCCLK
SP12	SDCDAT5		MSCDAT6
SP13	SDCDAT4		MSCDAT7
SP14			
SP15	SDCDAT3		
SP16	SDCDAT2		
SP17			
SP18			
SP19			



Security Classification		Compal Secret Data		Title	
Issued Date	2008/05/15	Deciphered Date	2009/05/15	RTS5158E Cardreader	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	KSWXX M/B LA-4611P Schematic
				Date:	Friday, August 01, 2008
				Sheet	26 of 47
				Rev	0.1

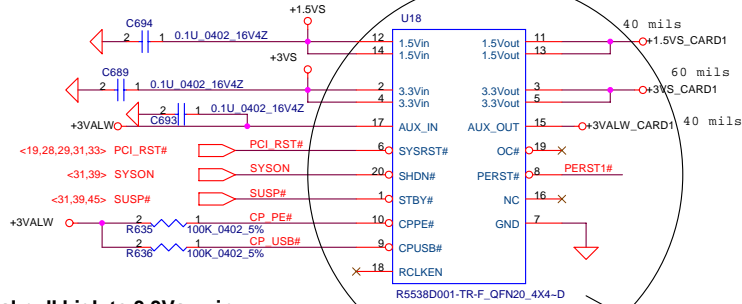
5		4		3	2	1		
D						D		
C						C		
B						B		
A						A		
<div>http://shop36609371.taobao.com/ QQ:52643956 笔记本维修资料</div>		Security Classification		Compal Secret Data		Title		
		Issued Date	2008/05/15	Deciphered Date	2009/05/15	None		
		THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
						Custom	KSWXX M/B LA-4611P Schematic	0.1
						Date:	Friday, August 01, 2008	Sheet 27 of 47
5		4		3	2	1		



Security Classification		Compal Secret Data		<i>Compal Electronics, Inc.</i>		
Issued Date	2008/05/15	Deciphered Date	2009/05/15			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
					KSWXX M/B LA-4611P Schematic	0.1
Date:				Friday, August 01, 2008	Sheet	29 of 47

New Card Power Switch

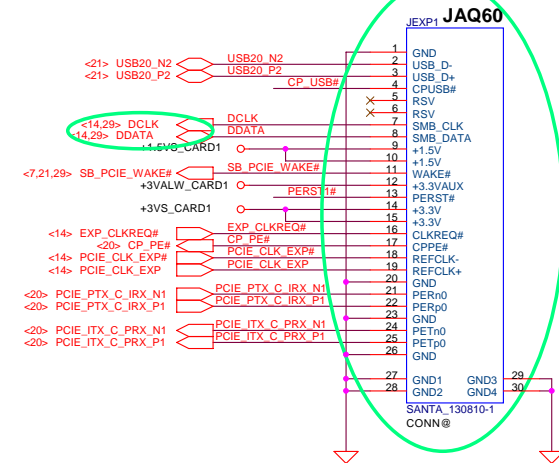
New Card



internal pull high to 3.3Vaux-in
EC need setting at Hi-Z & output Low

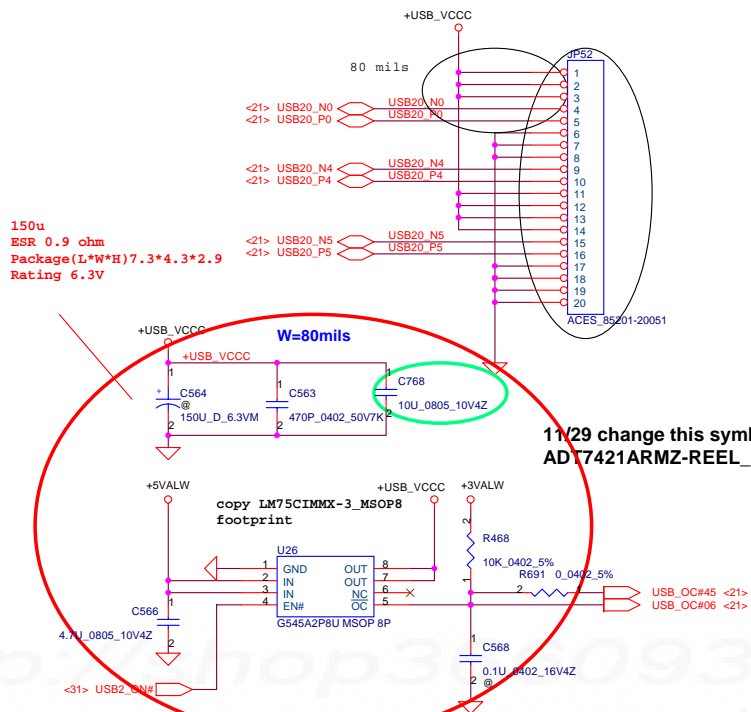
Change to GMT PartNumber

New Card Socket (Left/TOP)

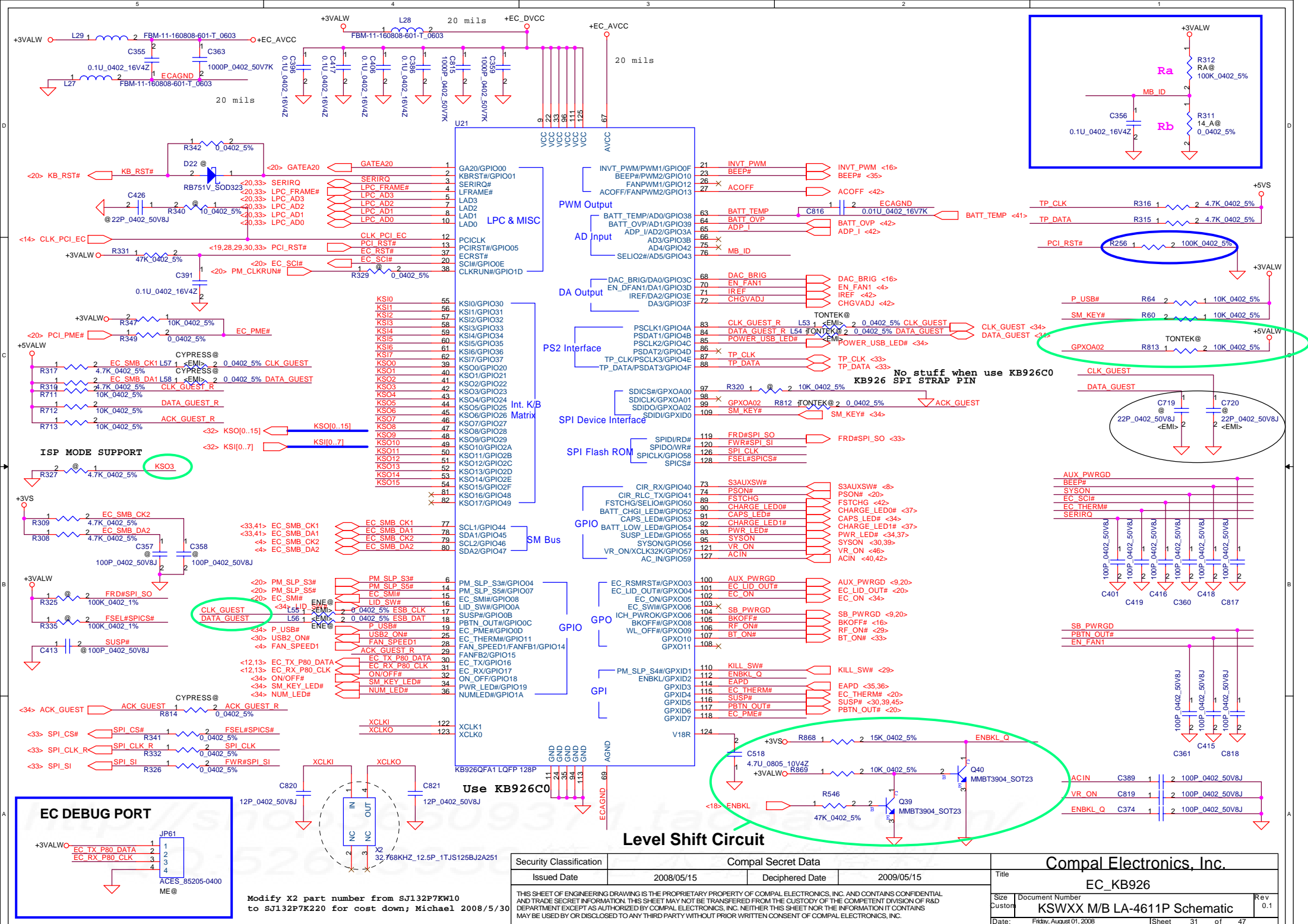


Update FootPrint from
SANTA_13181060-5_26P-T to
SANTA_130810-1_26P

USB IO Conn.

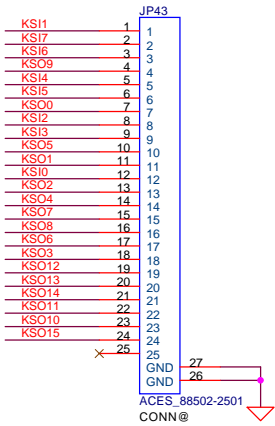


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title	
				NewCard & USB Connector	
				Size B	Document Number
				KSWXX M/B LA-4611P Schematic	
				Date	Rev
				Friday, August 01, 2008	0.1
				Sheet	30 of 47

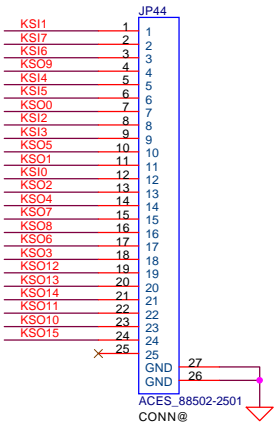


INT_KBD Conn.

For KSW91

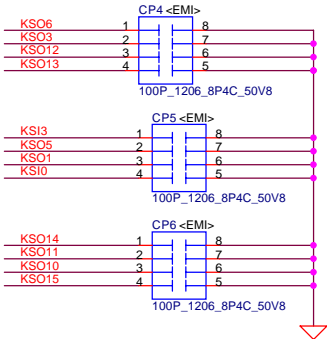
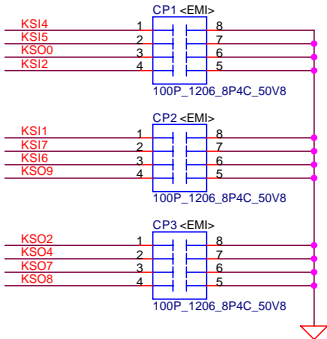


For KSW01



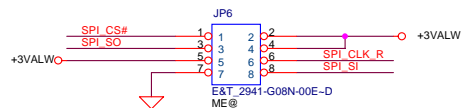
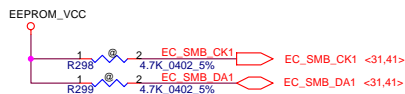
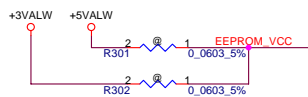
KSI[0..7] KSI[0..7] <31>
KSO[0..15] KSO[0..15] <31>

Delete C525~C548 SE071101J80 (100pF)
Add SI102101K80 (CP : 100pF)
(EMI Recommend)

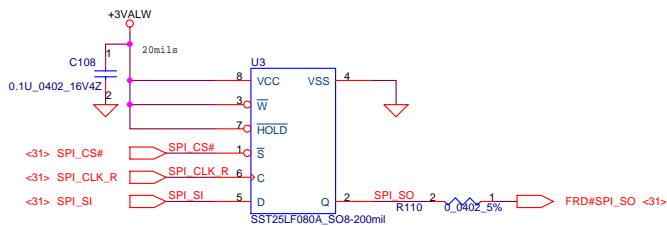


http://shop36609371.taobao.com/
QQ:52643956

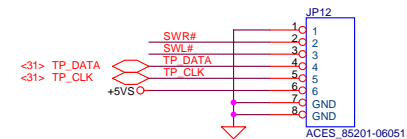
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/15	Deciphered Date	2009/05/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.1
				KSWXX M/B LA-4611P Schematic	
Date:		Friday, August 01, 2008		Sheet	32 of 47



8M SPI ROM



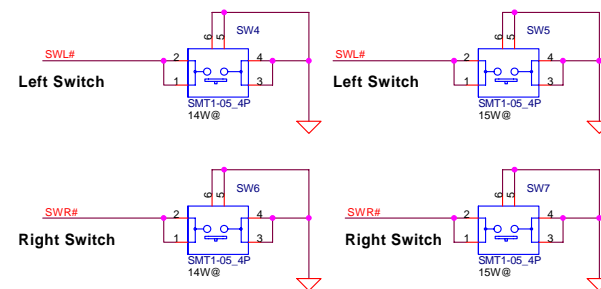
To TP/B Conn.



Update Footprint

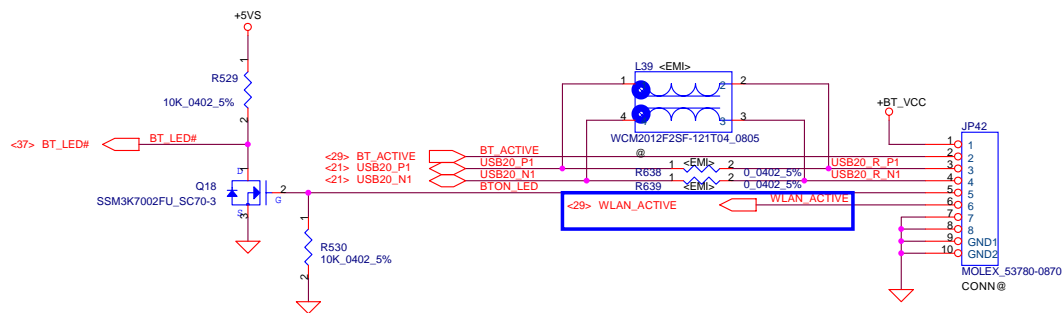
14W Use

15W Use

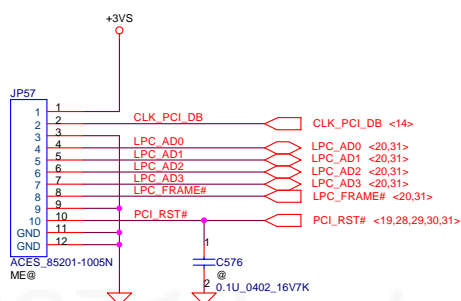


Bluetooth Conn.

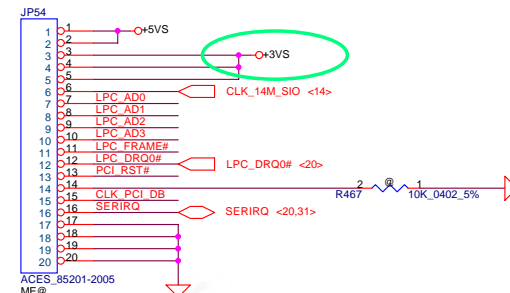
Need to check BT pin definition again!
9/20 modified this block



FOR LPC DEBUG PORT

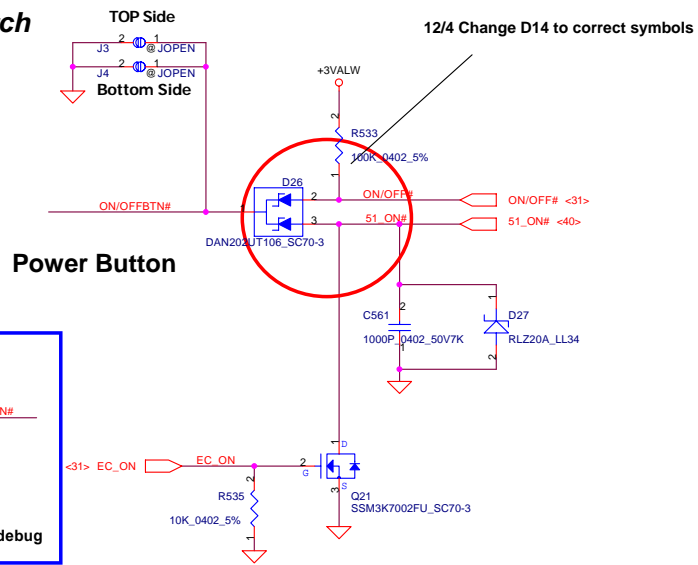


FOR LPC SIO DEBUG PORT

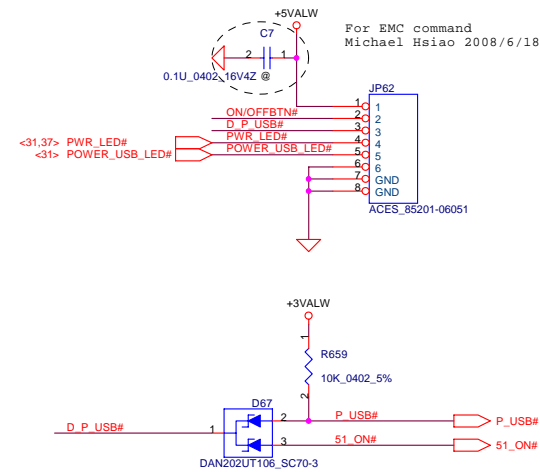


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/15	Deciphered Date	2009/05/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BIOS, I/O Port & K/B Connector	
Size	B	Document Number	KSWXX M/B LA-4611P Schematic		Rev
Date:	Friday, August 01, 2008	Sheet	33	of	47

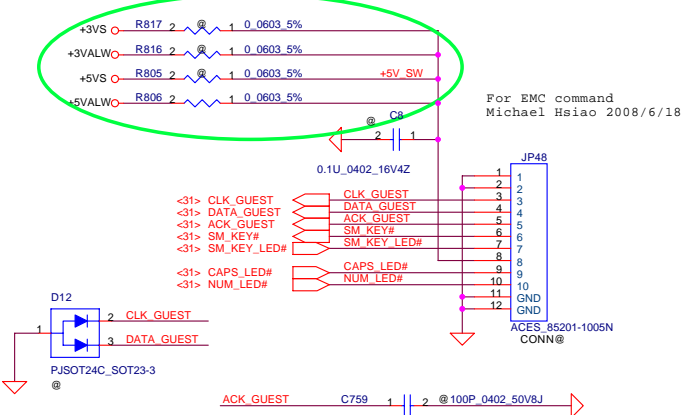
ON/OFF switch



Power USB Board Conn.

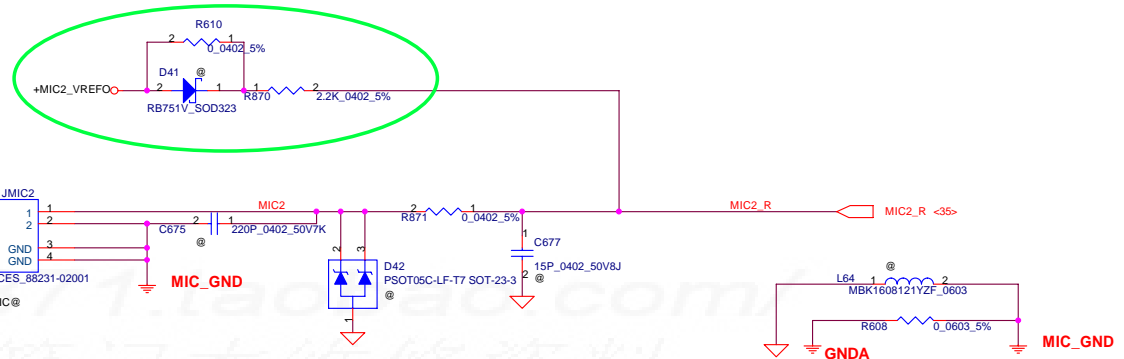


Function Board Conn.

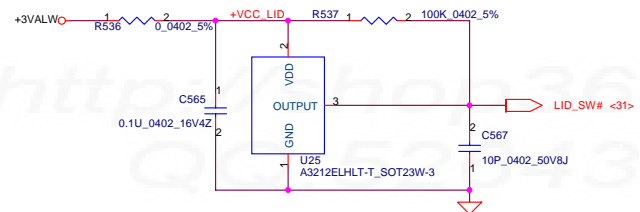


SINGLE INT MIC

05/29 close to codec



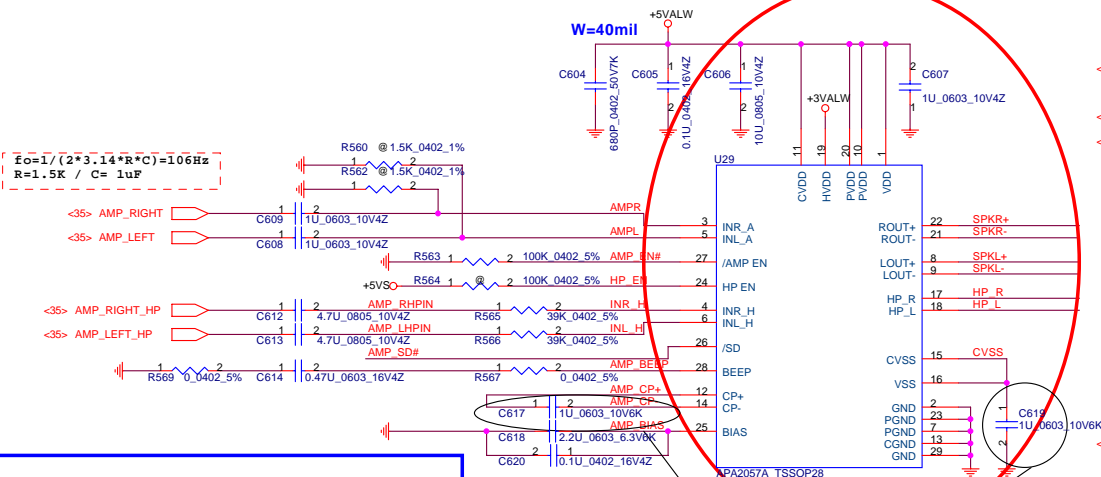
Lid Switch



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/15	Deciphered Date	2009/05/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				PWROK/LID/Front/IO Board	
Size B	Document Number	KSWXX M/B LA-4611P Schematic		Rev	0.1
Date:	Friday, August 01, 2008	Sheet	34 of 47		

APA2057 SPK/HP Amplifier

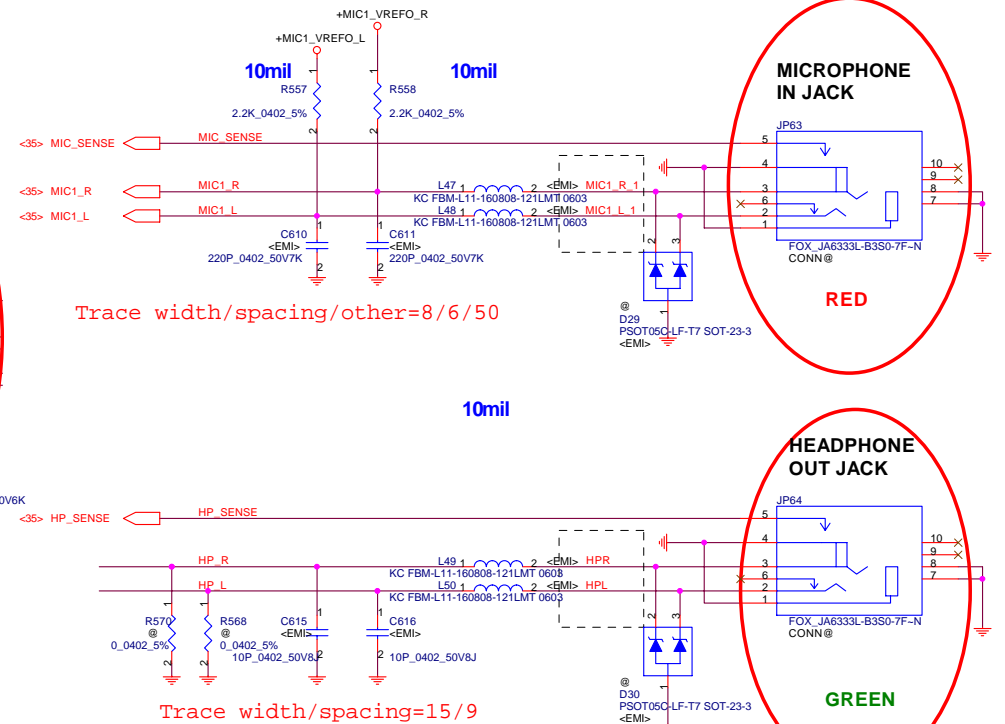
10/2 U6 APA2057A P/N:SA00001QD00



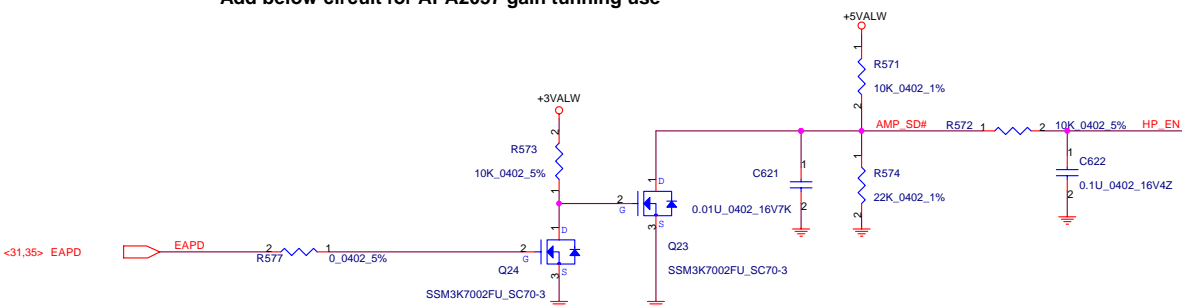
9/5 If implement AMP BEEP, Swap C641 and R524.
R524 change from 0 Ohm to 47K

IN_A Gain = 10dB (Internal Speaker)
IN_H Gain = 0dB (Headphone)

11/28 Modified to X5R
11/28 Change to SE080105K80



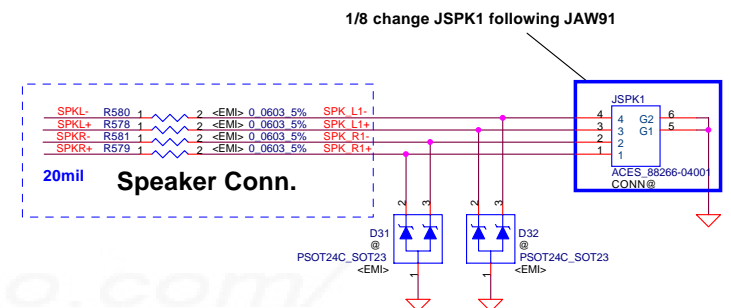
Add below circuit for APA2057 gain tuning use



Gain= 10dB

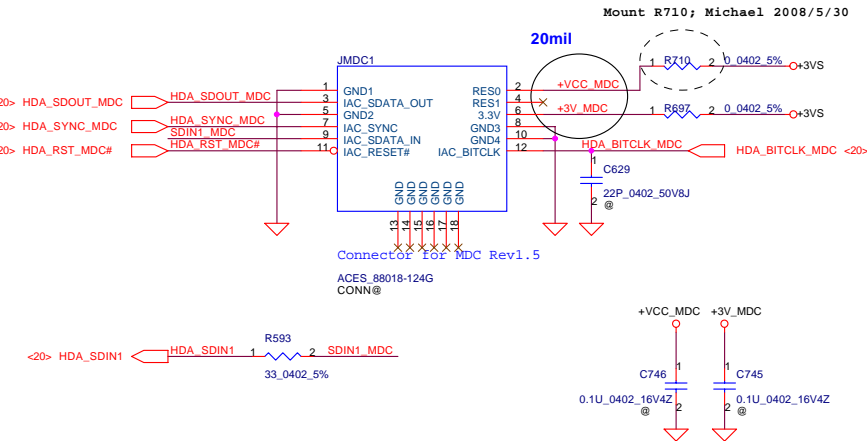
Gain (dB)	Low (V)	High (V)	Recommended (V)
10	3.45	3.51	3.48
11	3.56	3.62	3.59
12	3.68	3.73	3.70
13	3.80	3.85	3.82

+5VALW assume equal 5.1V
10 dB ---> $5.1 \times 220 / 320 = 3.5$

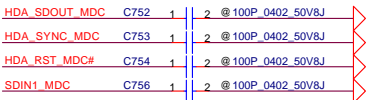


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2007/08/18	Deciphered Date	2008/8/18	Title	AMP/VR/Audio Jack	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custlbrn	KSWXX M/B LA-4611P Schematic	0.1
				Date:	Friday, August 01, 2008	Sheet 36 of 47

MDC Conn.

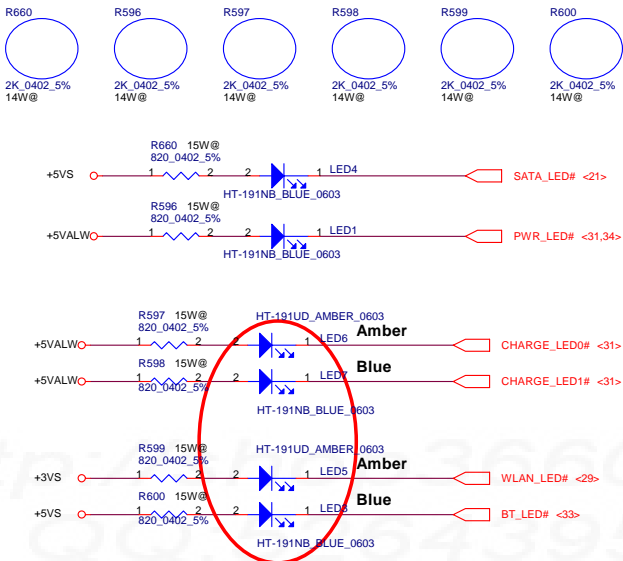


Please add these caps close to JMD1 as close as possible,



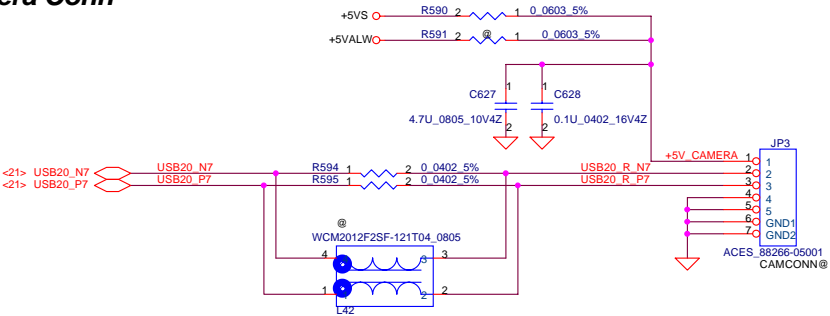
LED

05/30 Change R660,R596,R597,R598,R599,R600 to 820ohm for 15W@
05/30 Add R660,R596,R597,R598,R599,R600 to 2kohm for 14W@; follow JHXXX



12/7 Modified LED footprint to LED_HT-297UD-CB_4P
12/15 Modified to correct LED symbol!

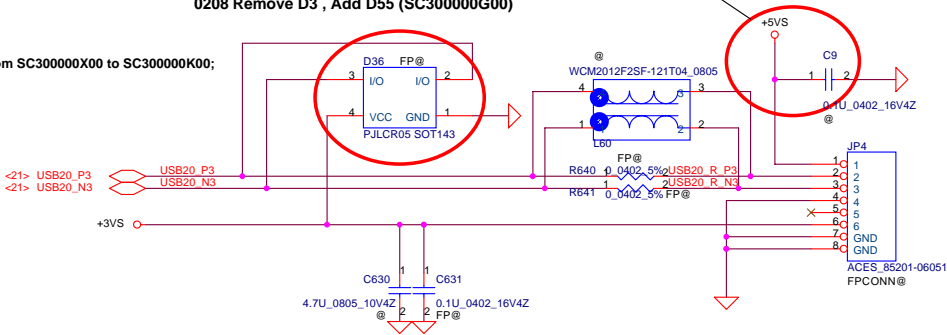
Camera Conn



Finger Print board

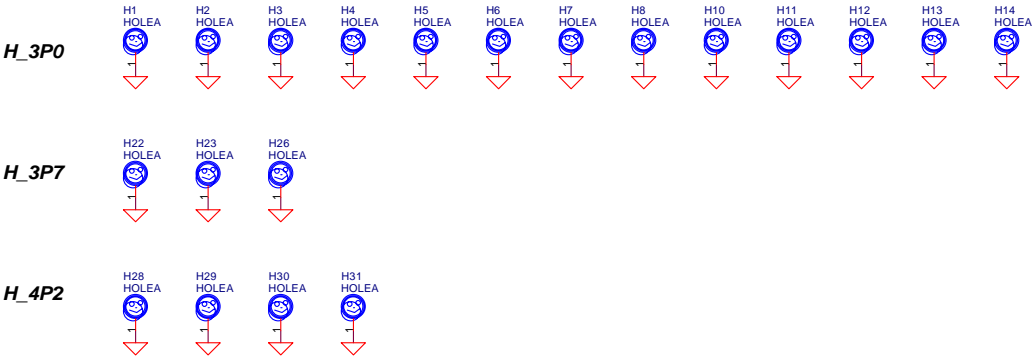
1/05 Modified D3 to SCA00000A00 For EMI 2/1 change JP4 pin 6 to +5VS for LTT FP use
0208 Remove D3 , Add D55 (SC300000G00)

05/26 Change D36 from SC300000X00 to SC300000K00;
Michael 2008/5/30



Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date		2008/05/15		Deciphered Date		2009/05/15		Title			
								LED/MDC/CAMERA			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								Size		Document Number	
								B		KSWXX M/B LA-4611P Schematic	
								Date:		Friday, August 01, 2008	
								Sheet		37 of 47	
								Rev		0.1	

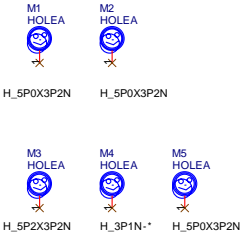
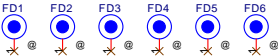
11/27 Add screw for layout request



2/22 change these from H_3P7 to H_3P8



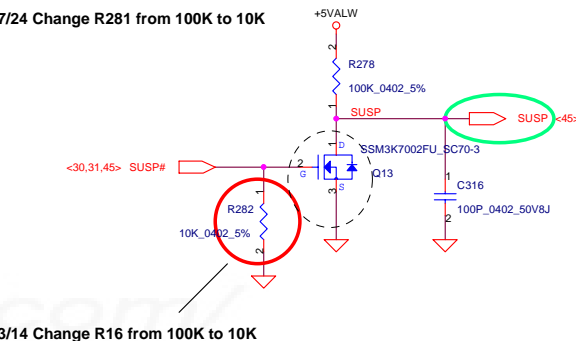
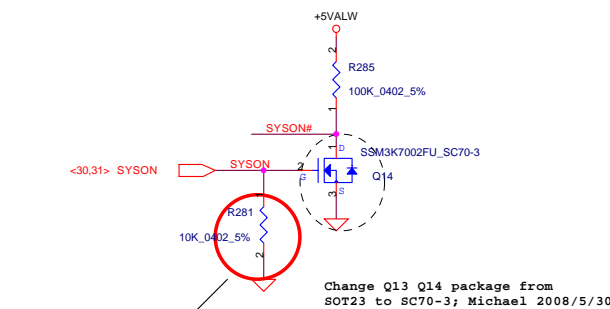
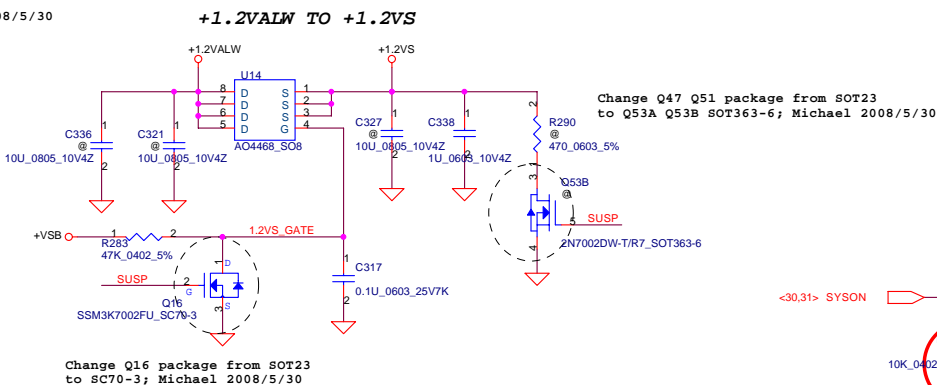
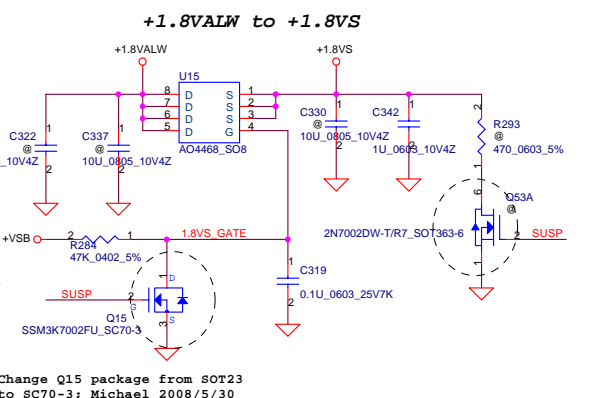
2/22 change these from H_3P2 to H_3P3



11/27 Add screw for layout request

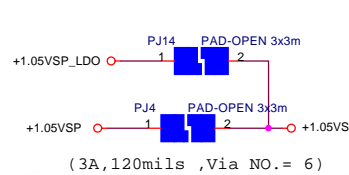
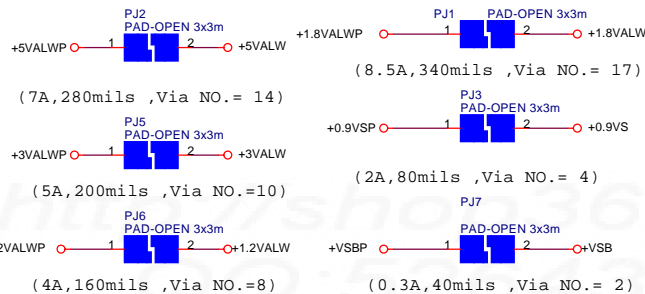
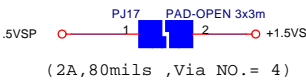
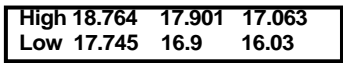
http://shop36609371.taobao.com/
QQ:52643956

Security Classification		Compal Secret Data		Compal Electronics, Inc.				
Issued Date	2008/05/15		Deciphered Date	2009/05/15		Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				FAN & Screw Hole				
				Size B	Document Number		Rev 0.1	
				JFWXX M/B LA-3961P Schematic				
Date:		Friday, August 01, 2008		Sheet	38 of 47			



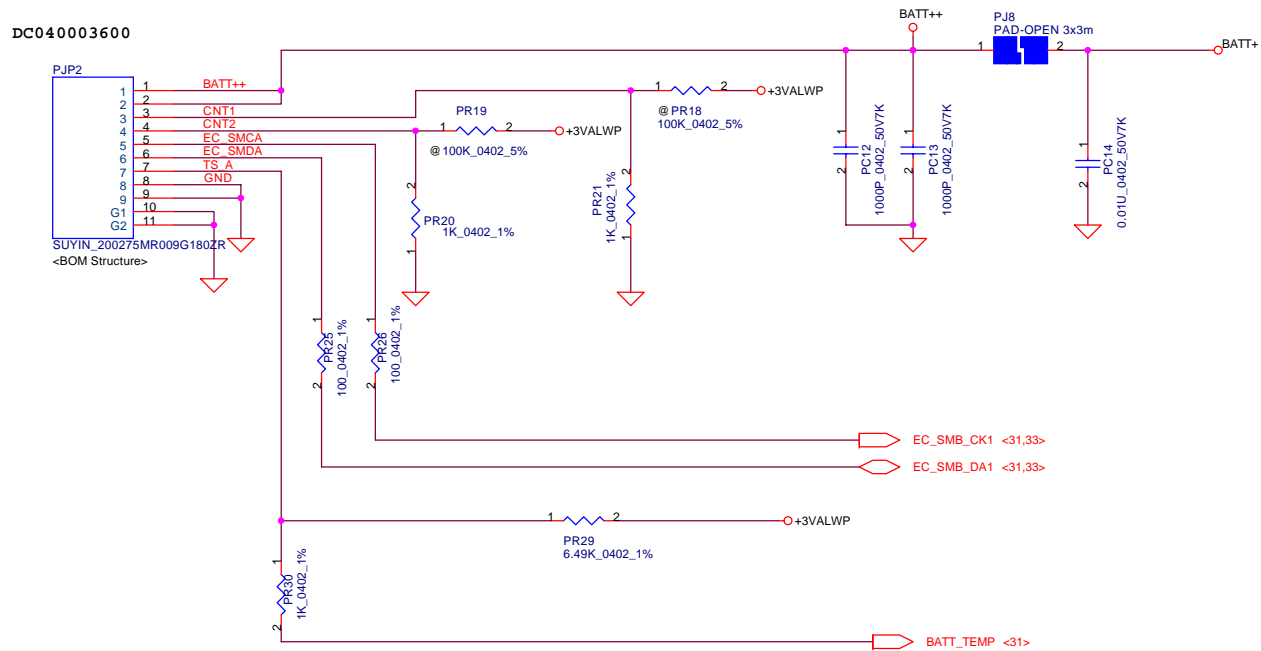
Security Classification		Compal Secret Data		Compal Electronics, Inc.						
Issued Date		2008/05/15	Deciphered Date		2009/05/15	Title				
						DC Interface				
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						Size B				
						Document Number				
						KSWXX M/B LA-4611P Schematic				
		Date:		Friday, August 01, 2008	Sheet	39 of 47				

④ SINGA_2DW-0268-B16

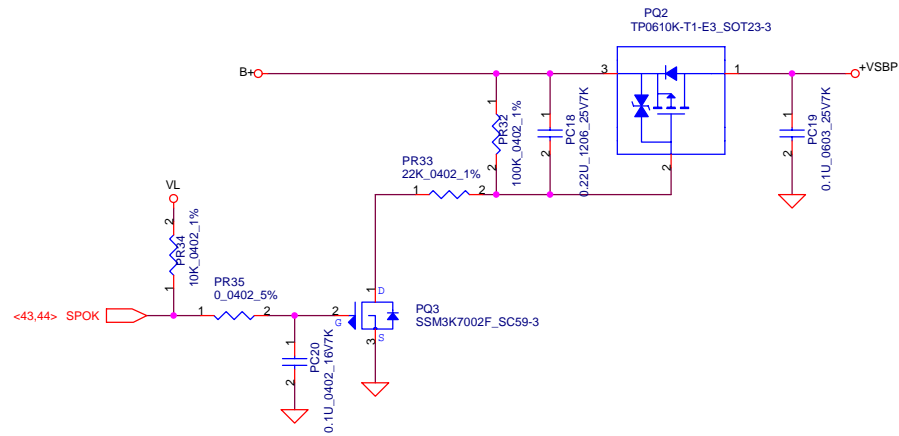
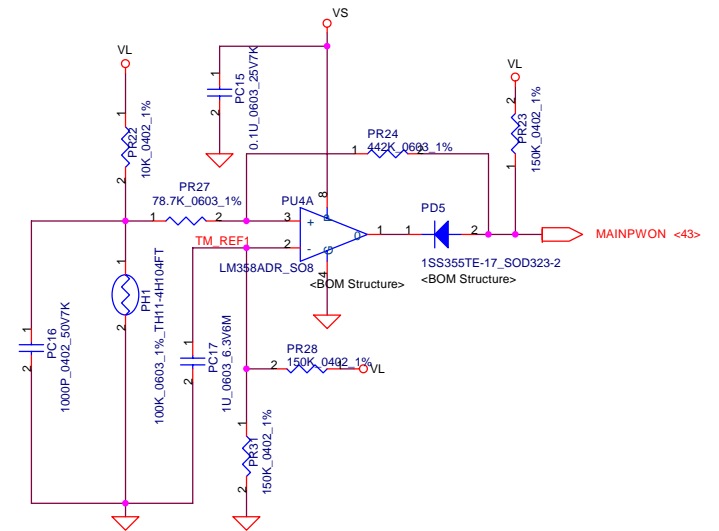


Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2005/10/17	Deciphered Date	2006/10/17	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				DCIN/DECTOR		
				Size B	Document Number	Rev. 0.2
				Date: Friday, August 01, 2008	Sheet 40 of 47	

DC040003600



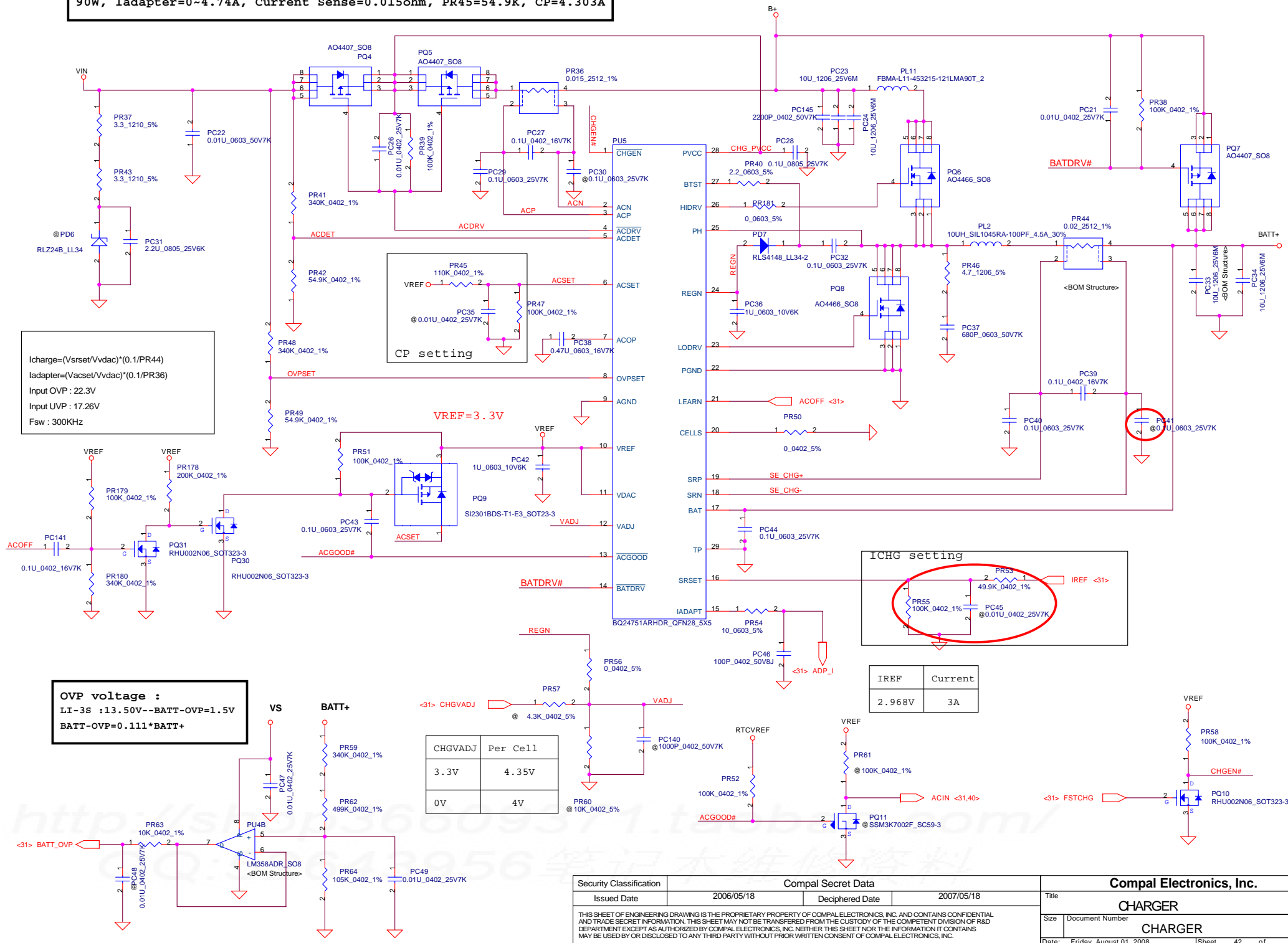
PH1 under CPU bottom side :
CPU thermal protection at 89 degree C
Recovery at 70 degree C

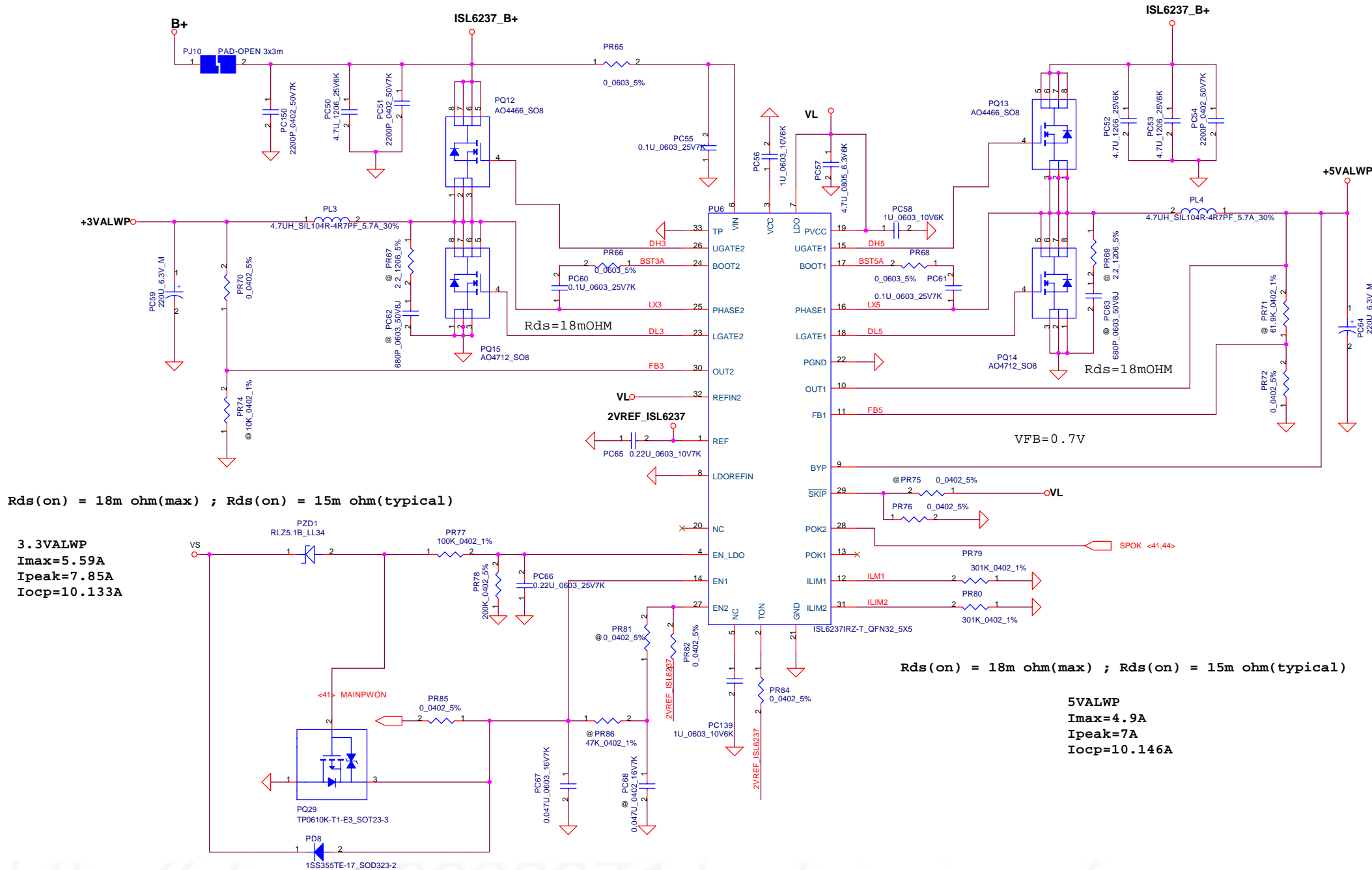


<http://shop36609371.taobao.com/>
QQ:52643956

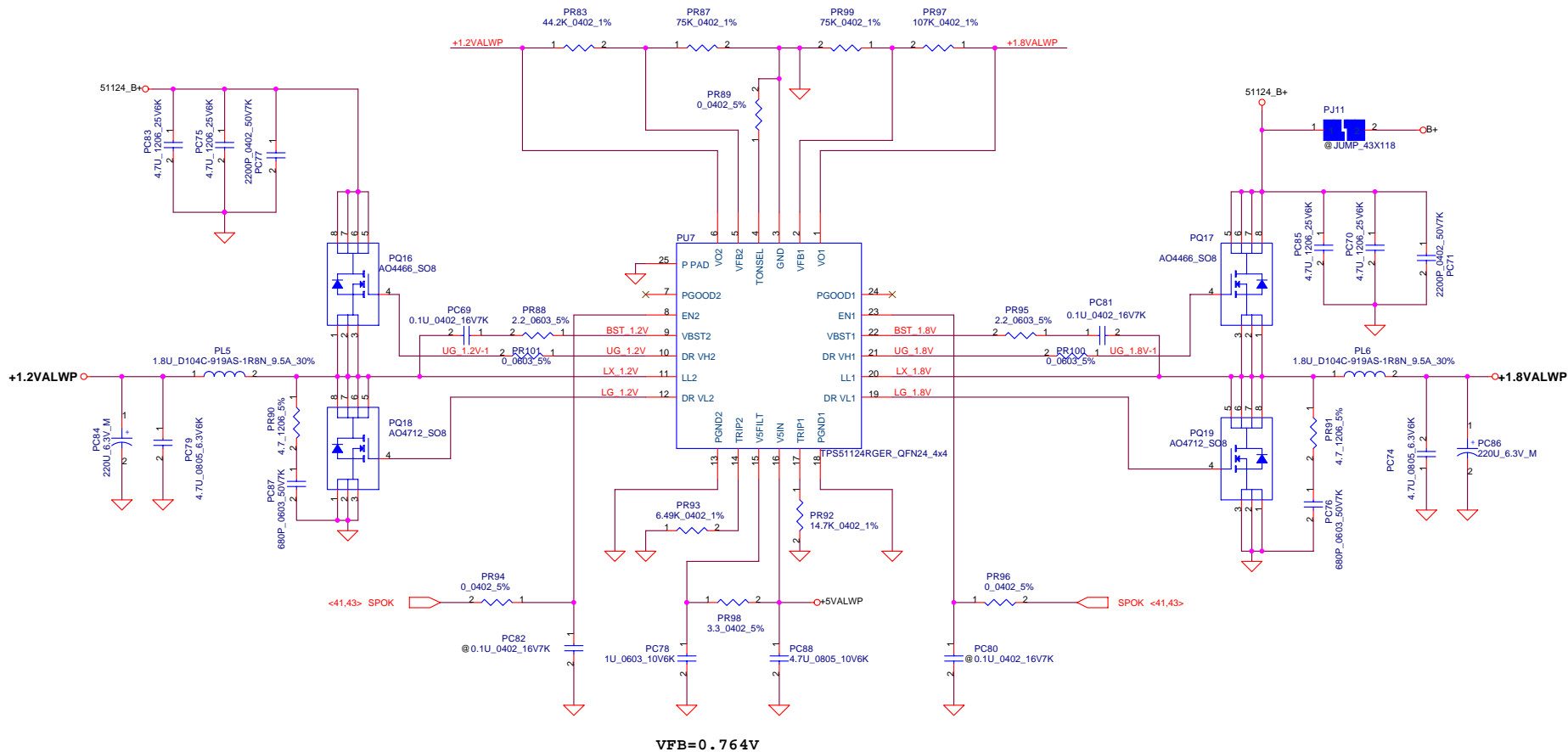
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/17	Deciphered Date	2006/10/17	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BATTERY CONN. / OTP	
Size B	Document Number	Date: Friday, August 01, 2008		Sheet 41 of 47	Rev 0.1

65W, Iadapter=0~3.42A, Current sense=0.015ohm, PR45=110K, CP=3.175A
90W, Iadapter=0~4.74A, Current Sense=0.015ohm, PR45=54.9K, CP=4.303A



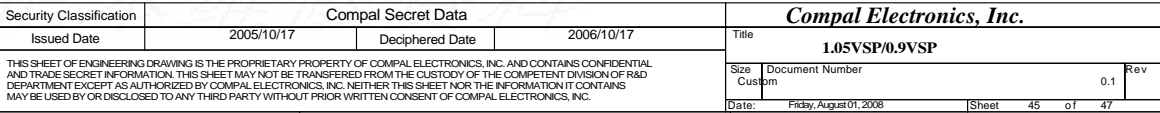


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/17	Deciphered Date	2006/10/17	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	Rev
				Date: Friday, August 01, 2008	Sheet 43 of 47



VFB=0.764V

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/17	Deciphered Date	2006/10/17	Title	1.5VP/1.8VP
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Rev 0.1
				Date: Friday, August 01, 2008	Sheet 44 of 47



page	Reason for change	Modify list
PVT	Add PR46 & PC37 for EMI request	
	Add bead at charger B+ for EMI request	
	Add PR141,PR144,PC116,PR155,PR157,PC125 for EMI power board band noise.	
	Add PC87,PR90,PR88,PR95,PR91,PC76 for EMI power board band noise.	
	Add PR113,PR117,PC98 for EMI power board band niose.	
	Change 1.8V from 1.836 to 1.853. SO PR97 to 107K for HW request	
	Change PR176 to 3.92K for adjust Loadline	
	Change PC97 to 220UF form EVT SMT MEMO	

<http://shop36609371.taobao.com/>
QQ:52643956 笔记本维修资料

Title		
<Title>		
Size	Document Number	Rev
Custom	Doc	<Rev Code>
Date:	Friday, August 01, 2008	Sheet 47 of 47